

WaveMaster® 8 Zi-A Series 4 GHz-45 GHz

Exceptional Performance and Superior Serial Data Analysis

- Up to 20 GHz on 4 Channels
- Up to 45 GHz Bandwidth at 120 GS/s
- Multi-Lane Serial Data Eye, Jitter and Crosstalk Analysis
- 14.1 Gb/s Hardware Serial Trigger Available



HIGH BANDWIDTH AND EXCEPTIONAL PERFORMANCE

Up to 45 GHz Bandwidth, 120 GS/s

High Bandwidth Real-time Oscilloscopes with Exceptional Performance

WaveMaster 8 Zi-A combines high bandwidth (45 GHz) and sample rate (120 GS/s) with superior signal fidelity performance and 20 GHz on all four input channels. Availability of models from 4 to 45 GHz with complete bandwidth upgradability throughout the entire product range makes it easy and affordable to stay current with emerging high-speed technologies and serial data standards.

WaveMaster 8 Zi-A is standard with the highest performance CPU in its class - an Intel® Core™ i7-2600 Quad-core (2.6 GHz per core, up to 3.8 GHz in Turbo mode) with 8 GB of RAM (upgradeable to 32 GB).

The oscilloscope's X-Stream™ II architecture fully leverages the CPU's speed in all aspects — 10-100 times faster analysis processing on maximum record lengths, instantaneous instrument responsiveness, and 20 times faster off-line data transfer.

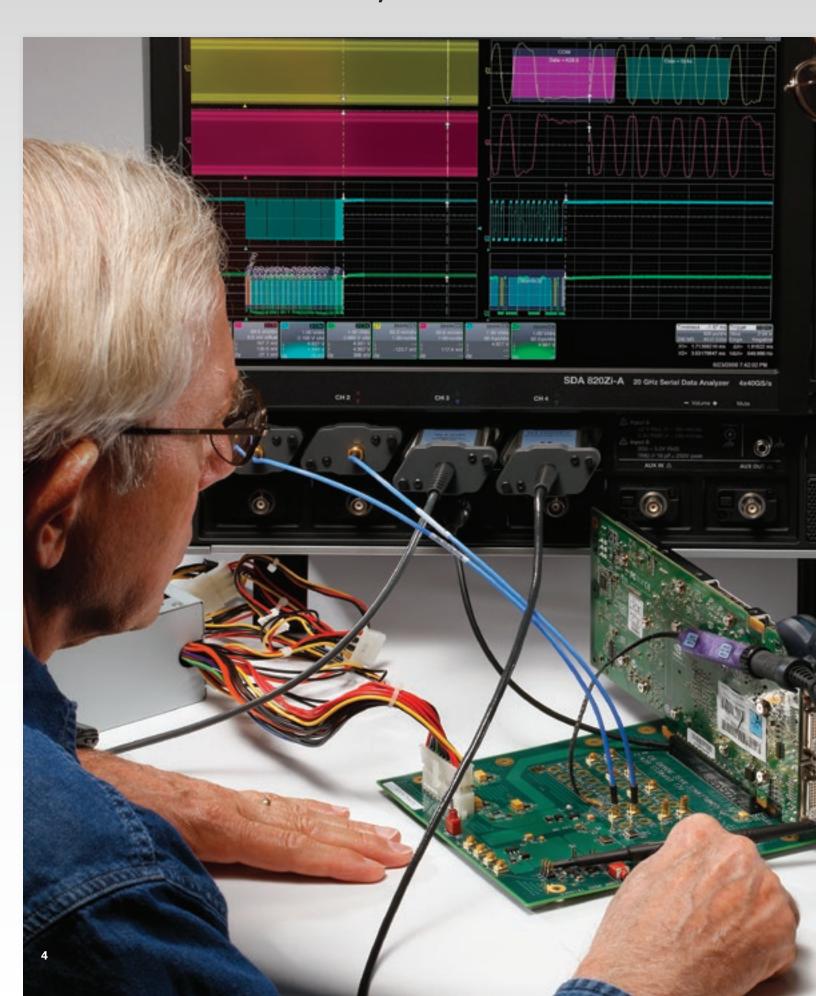
With SDAIII-CompleteLinQ Serial Data and Crosstalk Analysis software, you can simultaneously display four eye diagrams and calculate Tj, Rj, Dj, and Dj decomposition on four lanes or four locations in one lane. Use the EyeDrll and Virtual Probing toolsets to assist in analyzing lane interactions, including de-embedding crosstalk with S8p and S12p S-parameter files. Crosstalk analysis tools provide ability to measure vertical amplitude noise and deconvolve into Tn, Rn, Dn and Dn decomposition, and perform additional analysis to determine root cause of high noise.





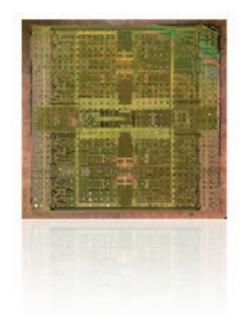
- 1. Industry leading performance—45 GHz bandwidth, 120 GS/s sample rate, 768 Mpts of analysis memory
- Widest bandwidth upgrade range (4–45 GHz) provides best investment leverage
- Intel[®] Core[™] i7-2600 Quad-core, 2.6 GHz (per core, up to 3.8 GHz in Turbo mode) CPU with 8 GB of RAM (upgradeable to 32 GB)
- **4.** Low Jitter Measurement Floor and highly stable timebase over long acquisitions
- **5.** Deepest toolbox with more measurements, more math, more power
- **6.** 15.3" widescreen (16×9) high resolution WXGA color touch screen display—25% larger than 12.1" displays
- 7. X-Stream II streaming architecture 10–100 times faster analysis and better responsiveness than other oscilloscopes
- 8. Crosstalk and Vertical Noise Analysis
- 9. SDAIII "LinQ" options provide four simultaneous eye diagrams and jitter calculations for multi-lane serial data link analysis, or for or single-lane, multiple location analysis
- 10. Eye Doctor™ II and Virtual Probe Signal Integrity Toolsets provide real-time de-embedding, emulation, and equalization on serial data channels and complex networks
- **11.** 325 MB/s data transfer rate from oscilloscope to PC with Teledyne LeCroy Serial Interface Bus (LSIB) option
- **12.** Up to 14.1 Gb/s Serial Trigger available 80-bit NRZ and 8b/10b Symbol triggering
- **13.** Largest selection of serial triggers and decoders—more than 19—available to provide a total system view
- 14. 50 Ω and 1 M Ω inputs with both ProBus and ProLink probe interfaces on all models provide support for every probe manufactured by Teledyne LeCroy without requiring external adapters or probe amplifiers

LEADING PERFORMANCE, INNOVATIVE TECHNOLOGY



World's Fastest Single-Chip ADC

Monolithic, custom-designed Silicon Germanium (SiGe) 40 GS/s Analog-to-Digital Converter (ADC) is the world's fastest single-chip ADC.



High Bandwidth, Upgradeable, Superior Serial Data Analysis Tools

Industry-Leading Real-Time Oscilloscope Series - Models up to 45 GHz

Teledyne LeCroy has utilized widely adopted and proven SiGe processes, custom 2nd generation ASIC designs, and 6th generation Digital Bandwidth Interleave (DBI) technology to achieve unprecedented real-time oscilloscope performance:

- 45 GHz
- 120 GS/s
- 768 Mpts/Ch Analysis Memory

20 GHz four channel performance is provided on all models from 20 to 45 GHz. In all cases, signal fidelity is pristine with exceptional rise time, step response, jitter measurement floor, and electrical noise performance. High effective number of bits (ENOB) over the complete operating frequency range, especially in the crucial mid-band, ensures the most noise-free display of signals.

Best Upgradeability and Investment Protection

By utilizing the same platform for all models from 4 to 45 GHz, we help you best protect your investment and give you future flexibility.

Up to 14.1 Gb/s Serial Trigger

The highest speed true-hardware serial trigger provides capability for 80-bit NRZ serial pattern, or 8b/10b symbol triggering.

A 6.5 Gb/s serial trigger is included standard with SDA 8 Zi-A models, and this may be upgraded to 14.1 Gb/s capability.

Either serial trigger may be added to WaveMaster 8 Zi-A and DDA 8 Zi-A models.

Multi-Lane Serial Data and Crosstalk Analysis

The world's first and only serial data analysis software to provide four simultaneous eye diagrams, jitter measurements, and jitter analysis.

Crosstalk analysis provides vertical noise measurements and crosstalk debug tools. Add Teledyne LeCroy's Eye Doctor™ II and Virtual Probe

Signal Integrity Tools for more power.

Analyze full record lengths to better understand low frequency system behaviors. Unique jitter decomposition algorithms are provided to better understand system behaviors.

THE BEST HIGH BANDWIDTH INVESTMENT

Superior High Bandwidth Performance

As memory and sample rate can be interleaved, so can bandwidth. Using high-performance technologies and digital signal processing (DSP), Teledyne LeCroy uses high-speed SiGe analog components comfortably within their rated bandwidth range (20+ GHz) while providing additional bandwidth on one or two channels using 6th generation Digital Bandwidth Interleaving (DBI). This approach provides 4 channels at 20 GHz, 2 channels at 30 GHz, and 1 channel at 45 GHz, with better signal fidelity compared to "stretching" of components beyond their rated bandwidth. It also best leverages proven technologies with known and high reliability to minimize up-front purchase costs.

Learn More

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Proven SiGe Components Ensure High Performance

Silicon Germanium (SiGe) is the most widely adopted and deployed semiconductor fabrication process with many years of commercial deployment. Additionally, it has none of the thermal conductivity, reliability, yield, cost, and other concerns that captive, in-house processes must contend with.

Low Jitter Measurement Floor and High Timebase Stability

An exceptionally accurate and stable timebase is incorporated for the best possible jitter measurement accuracy. Teledyne LeCroy provides highly stable measurements at full (768 Mpts) record lengths, simplifying debug of low frequency events.

Widest Bandwidth Upgrade Range: 4-45 GHz



SDA 845Zi-A



WaveMaster 820Zi-A

All WaveMaster 8 Zi-A oscilloscopes are implemented with a single hardware platform. To extend bandwidth beyond 20 GHz, Teledyne LeCroy has leveraged DBI technology to minimize initial costs—the module that doubles the bandwidth slides into a separate slot in the WaveMaster 8 Zi-A platform.

Best Investment Protection

From the perspective of bandwidth, sample rate, processing speed, responsiveness, display size, and range of capability, the WaveMaster 8 Zi-A platform is clearly superior and will remain so for many years to come. With the widest bandwidth upgrade range, an engineer who is working on current generation technologies today can confidently know that WaveMaster 8 Zi-A will support the next generations of technology several years from now.



World's Fastest Single-chip ADC

The monolithic 40 GS/s ADC is the fastest single-chip ADC. Compared to other approaches that use multiple ADC chips per channel, or single-chip ADCs with more than 100 interleaved converters, the Teledyne LeCroy approach is a simpler,

more elegant solution for maintaining proper timing, phasing, and offset between the on-chip ADCs. The result is vastly improved spurious free dynamic range (SFDR) compared to other oscilloscopes in its class.

High-speed Memory

Custom high-speed memory chips on multiple memory plug-in cards achieve up to 256 Mpts/Ch (or up to 768 Mpts/Ch interleaved with some models and options). X-Stream II architecture ensures fast and complete processing of full record lengths with no limitations on analysis memory.

X-STREAM II FAST ANALYSIS AND RESPONSIVENESS



Deep Insight for Analysis

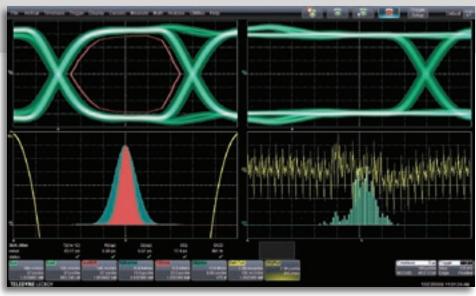
An oscilloscope's operating performance is as important as its electrical performance. The best operating performance comes from a design that seamlessly integrates the operating system, the hardware processor and the waveform processing method. Each component is important but only the Teledyne LeCroy's X-Stream II waveform processing method unleashes amazing speed performance and no compromise in responsiveness. The result is a drastic reduction in calculation time which, when paired with Teledyne LeCroy's deep measurement and analysis toolbox, allows an engineer to generate deep insight about their design.

Teledyne LeCroy —The Analysis Memory Leader

Teledyne LeCroy has found a way to make long acquisition memory seamless and pain free to use. The WaveMaster 8 Zi-A Series' proprietary X-Stream II architecture supports capturing, zooming, measuring and analyzing multiple waveforms at up to 768 Mpts deep. WaveMaster 8 Zi-A's proprietary architecture design is augmented with an Intel® Core™ i7-2600 Quad-core processor (15.2 GHz GHz effective clock rate in Turbo mode), high-speed serial data buses, Windows® 7 64-bit OS and 8 GB of RAM standard (upgradeable to 32 GB). What you experience is processing speed 10-100x faster compared to other oscilloscopes in this class.

Instantaneous Responsiveness

With WaveMaster 8 Zi-A oscilloscopes you will experience remarkable responsiveness. Acquiring and manipulating the longest record



WaveMaster 8 Zi-A excels at performing complex calculations on long waveforms, enabling users to gain waveform insight with confidence. Here, a 40 Mpts PCIe Gen1 waveform acquisition is acquired and fully analyzed in a matter of seconds—nearly 100x faster than competitive oscilloscopes.

lengths and performing the most complex WaveShape Analysis are all easily handled at the same time, unlike competitive oscilloscopes that become painfully slow to respond when long memory is applied. Bottom line: oscilloscopes no longer need to carry a penalty for operating with long memory.

Fast Off-line Data Transfer

When the application calls for postprocessing data off-line, an optional Teledyne LeCroy Serial Interface Bus (LSIB) high-speed 325 MB/s option provides data transfer 20–100x faster than any other test instrument. For remote control, WaveMaster 8 Zi-A is Class C compliant with the LXI standard, the latest industry standard for Ethernet remote control operation. WaveMaster 8 Zi-A supports standard LXI features such as a LAN interface, VXI11 Discovery, a web server and IVI-C & IVI-COM drivers.

X-Stream II Architecture

Optimized for Fast Throughput

X-Stream II architecture enables high throughput of data—even when the oscilloscope is performing multiple 100 Mpts (or larger) waveforms.

X-Stream II uses variable waveform segment lengths to enable all processing intensive calculations to take place in fast CPU cache memory, thus improving calculation speed and efficiency. The result—10–100x faster processing compared to other oscilloscopes.

Learn More

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Optimized for Long Memory

X-Stream II has no analysis memory length restrictions, regardless of analysis type, since the variable waveform segment length can always be limited to a size that can fit in CPU cache memory. Other oscilloscopes with conventional architectures cannot make this claim, and often have limitations on analysis memory of 5–20% the length of their acquisition memory under the best conditions.

Optimized for Responsiveness

By dynamically allocating buffers to maximize memory availability, the WaveMaster 8 Zi-A Series embodies the fastest front panel responsiveness. Oscilloscopes from other manufacturers can suffer from annoying delays during simple zoom operations, but not WaveMaster 8 Zi-A.

Learn More

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SERIAL DATA PHYSICAL LAYER COMPLIANCE TESTING

Compliance Testing Key Features

- Full support for Transmitter, Receiver and Signal Integrity testing
- Transmitter Testing (SDA 8 Zi-A Serial Data Analyzer)
- QualiPHY Compliance
 Test Packages simplify
 test and reporting
- Connection Diagrams ensure the proper testing configuration
- Report Generation includes all of the testing values and the appropriate limits
- Stimulate the DUT for transmitter testing using the PeRT³
- Receiver Testing (PeRT³)
 - BER Generator & Detector
 - Multichannel Operations
 - Protocol Support
 - Jitter Tolerance Testing
 - Integrated Pattern Generator
 - Protocol Level Error Detection
- Stress Injection Capable
- SSC Support
- Pre-emphasis
- Input Sensitivity Testing
- Impedance Testing (SPARQ)
 - SPARQ satisfies numerous transmitter, receiver, cable and fixture compliance testing requirements for standards such as:
 - SATA
- · SAS
- USB
- Fibre
- PCI Express
- Channel
- HDMI
- DisplayPort



The combination of the SDA 8 Zi-A Serial Data Analyzer, the PeRT³ and the SPARQ provides the most comprehensive solution for serial data compliance testing. These three pieces of equipment enable a full suite of physical layer compliance testing and debugging ability that will guarantee the best signal integrity for your serial data signals.

Transmitter Compliance Testing

The addition of a QualiPHY software option to the SDA 8 Zi-A oscilloscope constitutes the ideal instrument for physical layer compliance testing. QualiPHY reduces the time and effort needed to perform compliance testing on a wide array of high-speed serial buses by automating the process with connection diagrams and a comprehensive report of results including screenshots. QualiPHY

uses all of the powerful oscilloscope features to perform the compliance test quickly and easily.

Receiver Testing

The Protocol Enabled Receiver
Transmitter Tolerance Tester (PeRT³)
fills the space between physical layer
test and protocol layer test, providing
a new, more intelligent capability for
performance testing of receivers and
transmitters. Designed to meet the
test needs of engineers working with
serial data transceivers and other
high-speed serial data communication
systems, the Teledyne LeCroy PeRT³
test system is not just a new
instrument; it's an entirely new
instrument class.

Complete End-to-end Testing

When using the SDA 8 Zi-A oscilloscope for transmitter only testing, the user is still required to stimulate the product under test to output the required test patterns.

Likewise, when using the PeRT³ for receiver only testing, the specifications require the user to calibrate the jitter output sources prior to performing the receiver test. When combining the SDA 8 Zi-A oscilloscope with the PeRT³, not only can each of these needs be met, but all of the testing can be automated and included in a single test report.

Automated Compliance Testing for the Following Standards:

- 10/100/1000 BaseT ENET
- DisplayPortSAS
- USB 2.0
- HDMI
- MIPI D-PHY
- HDIVI
- DDR2 / DDR3
- SATA
- PCI Express
- USB 3.0

By utilizing the power of the SDA 8 Zi-A, the PeRT³ and the SPARQ, the most comprehensive serial data testing can be performed with unparalleled simplicity.

Transmitters, receivers, cables and fixtures can all be characterized to ensure compliance.

Signal Integrity Testing

The SPARQ Signal Integrity Network Analyzer performs a wide range of compliance tests, including: Impedance, Return Loss, Impedance Imbalance, Insertion Loss, Crosstalk (Near- and Far-end), Differential-to-common-mode conversion, Common-to-differential-mode conversion, Intra-pair Skew and Voltage Transfer functions. All measurements can be made in differential-mode, common-mode or single-ended, as applicable.

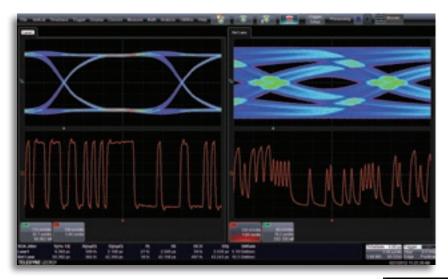
Data Rate Configuration Chart

Standard	Bit Rate	Minimum Bandwidth	Recommended Oscilloscope
PCI Express Gen1	2.5 Gb/s	6 GHz	SDA 808Zi-A or Above
ExpressCard	2.5 Gb/s	8 GHz	SDA 808Zi-A or Above
InfiniBand	2.5 Gb/s	8 GHz	SDA 808Zi-A or Above
Serial Rapid I/O	2.5 Gb/s	8 GHz	SDA 808Zi-A or Above
DisplayPort 1.1	2.7 Gb/s	8 GHz	SDA 808Zi-A or Above
HyperTransport 2.0	2.8 Gb/s	8 GHz	SDA 808Zi-A or Above
SAS Gen1	3 Gb/s	8 GHz	SDA 808Zi-A or Above
Serial Rapid I/O	3.125 Gb/s	8 GHz	SDA 808Zi-A or Above
SGMII	3.125 Gb/s	8 GHz	SDA 808Zi-A or Above
XAUI	3.125 Gb/s	8 GHz	SDA 808Zi-A or Above
FireWire	3.2 Gb/s	8 GHz	SDA 808Zi-A or Above
HDMI 1.4	3.4 Gb/s	8 GHz	SDA 813Zi-A or Above
SATA Gen2	3 Gb/s	10 GHz	SDA 808Zi-A or Above
DDR4	4 GT/s	10 GHz	SDA813Zi-A or Above
Fibre Channel 4GFC	4.25 Gb/s	13 GHz	SDA 813Zi-A or Above
Serial Rapid I/O	4.25 Gb/s	13 GHz	SDA 813Zi-A or Above
InfiniBand	5 Gb/s	13 GHz	SDA 813Zi-A or Above
PCI Express Gen2	5 Gb/s	13 GHz	SDA 813Zi-A or Above
PCI Express Gen3	8 Gb/s	13 GHz	SDA 813Zi-A or Above
Serial Rapid I/O	5 Gb/s	13 GHz	SDA 813Zi-A or Above
HyperTransport 3.0	5.2 Gb/s	13 GHz	SDA 813Zi-A or Above
USB 3.0	5 Gb/s	13 GHz	SDA 813Zi-A or Above
DisplayPort 1.2	5.4 Gb/s	16 GHz	SDA 816Zi-A or Above
GDDR5	6 Gb/s	16 GHz	SDA 816Zi-A or Above
SAS Gen2	6 Gb/s	16 GHz	SDA 816Zi-A or Above
SATA Gen3	6 Gb/s	16 GHz	SDA 816Zi-A or Above
Serial Rapid I/O	6.25 Gb/s	16 GHz	SDA 816Zi-A or Above
HyperTransport 3.1	6.4 Gb/s	16 GHz	SDA 816Zi-A or Above
QPI (Quick Path Interconnect)	6.4 Gb/s	16 GHz	SDA 816Zi-A or Above
10GBase-KR	10.3125 Gb/s	20 GHz	SDA 820Zi-A or Above
SFI/SFP+	10.3125 Gb/s	20 GHz	SDA 820Zi-A or Above
CEI-11	11Gbps	25 GHz	SDA 825Zi-A or Above
SAS12	12 Gb/s	25 GHz	SDA 825Zi-A or Above
InfiniBand	25.78125 Gb/s		SDA 830Zi-A,
CEI-25/28	25–28 Gb/s	30 to 65 GHz	LabMaster 9 Zi-Å or LabMaster 10 Zi up to 65 GHz



The SPARQ can perform all serial data compliance tests currently made with TDR or VNA instruments—only easier.

SDAIII-COMPLETELING SERIAL DATA ANALYSIS PRODUCTS

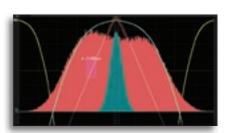


The Teledyne LeCroy SDAIII-CompleteLinQ
Serial Data Analysis products contain
multi-lane eye and jitter analysis,
LaneScape™ comparison modes, vertical
noise measurements, and crosstalk analysis
tools. These capabilities provide the
deepest insight into the behavior of
multi- or single-lane serial data systems.

SDAIII Core Toolset

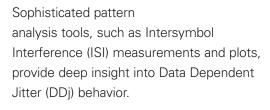
Teledyne LeCroy provides the most complete toolset in the industry for jitter measurements and eye diagram/ jitter analysis. Rj and Dj are separated and Dj is decomposed using one of three dual-Dirac algorithms. Eye diagrams containing all acquired unit intervals are rendered 10-100x faster than competitive systems. Eye diagram analysis tools, such as the extrapolated

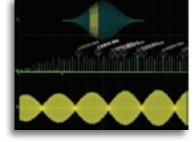
IsoBER plot, aid insight. Multiple additional tools, such as Tracks, Histograms, and Spectrum waveforms, enhance the understanding of jitter causes.



Rj+BUj Analysis

Eve with IsoBER Τį Total Jitter DDj Analysis with ISI Plot Di Ri Random Jitter Deterministic Jitter **BUi DDi** Data Dependent Jitter Bounded Uncorrelated Jitter Pi **OBUj DCD** ISI Other Bounded **Duty Cycle** Periodic Jitter Intersymbol Uncorrelated Jitter Distortion Interference





Pj Analysis



Three Jitter Methodologies

Choose from three dual-Dirac models to separate jitter into total, random and deterministic components (Tj, Rj, Dj). The Spectral Rj Direct method determines Rj directly from the jitter spectrum, and is the most used algorithm. Spectral Rj+Dj CDF Fit follows the FibreChannel MJSQ model. In situations where large amounts of crosstalk/BUj raise the spectral noise floor, the NQ-Scale method will provide more accurate separation of Rj and Dj, and therefore more accurate Tj results.

OPTIONAL SDAIII UPGRADES

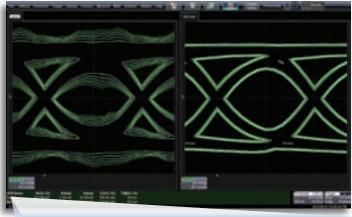
Measure up to 4 Lanes Simultaneously

"LinQ" products provide extensive multi-lane analysis capabilities. Quickly understand lane-to-lane differences in jitter measurements, eye diagrams, and jitter analysis. Perform aggressor on/off analysis, and see the results from both scenarios simultaneously. Save the analysis of a particular scenario to the Reference Lane, and configure a LaneScapeTM Comparison mode to compare the Reference to either one, two or all lanes. Each "lane" can be a different serial data lane, or a different analysis of data from a single serial data lane - ideal for comparing different equalization schemes (using Eye Doctor II option)

or examining system behaviors at different locations in the lane (using probes or the VirtualProbe option).

Vertical Noise and Crosstalk

The Crosstalk and CrossLinQ packages provide vertical noise measurements and crosstalk analysis tools for



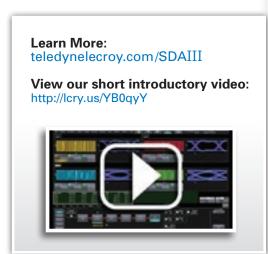
SDA Noise Tn(1e-12) EH(1e-12) EW(1e-12) Rn(sp) Dn(sp) lane1 131 28 mV 7.18 mV 34.39 mV 105.04 mV 125 ps 33.38 mV 24.93 mV Ref Lane 646 uV 172.41 mV 131 ps

CompleteLinQ Does it All

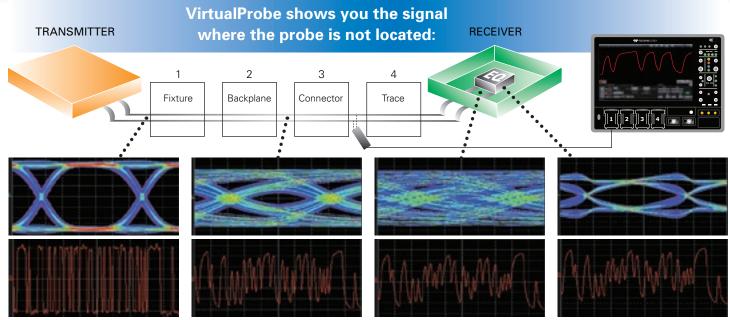
The CompleteLinQ user interface framework provides easy access to all features described above, and also integrates EyeDoctorII and VirtualProbe capabilities for Tx/Rx equalization and fixture/channel de-embedding/emulation. Order SDAIII-CompleteLinQ to equip your oscilloscope with all of Teledyne LeCroy's Serial Data Analysis and Signal Integrity tools.



complete aggressor/victim analysis. Use one of three dual-Dirac models to measure and separate noise into total (Tn), random (Rn) and deterministic (Dn) components, and further decompose Dn into Intersymbol Interference Noise (ISIn) and Periodic Noise (Pn). Only Teledyne LeCroy performs this analysis on real-time oscilloscopes. Similar to jitter analysis, noise can be viewed as a noise track, histogram and spectrum, providing insight into the vertical noise resulting from coupling to other active serial data lanes or other interference sources. The Crosstalk Eye shows the probabilistic extent of noise both inside and outside the eye, quickly showing the impact of excessive noise that is not possible to see in a traditional eye diagram.



EYEDOCTOR™II AND VIRTUALPROBE SIGNAL INTEGRITY TOOLS



Virtually probe the signal at the transmitter with the fixture present, and then de-embed its effects form the measurement.

View the signal between structures to understand losses, ISI and crosstalk caused by backplanes, interconnects and connectors.

See what the eye looks like at the receiver - even if it is not in reach of a differential probe.

Use EyeDoctor to open the eye by modeling CTLE, FFE and DFE equalizers used by your receiver.

As signal speeds and data rates continue to rise, signal integrity effects such intersymbol interference (ISI) and crosstalk become more prevalent and challenging. Use Teledyne LeCroy's Advanced Signal Integrity tools to transform your measured signal to include the effects of de-embedding, emulation and equalization algorithms.

De-embed, Equalize and Emulate with EyeDoctorII

Curious to know what your signal would look like without fixture effects? Do you need to understand how ISI and crosstalk of a modeled channel will affect your jitter margin? Or are you seeking to determine which equalization schemes will do the best job of opening a closed eye? The EyeDoctorII package includes easy configuration of basic de-embed/emulation scenarios, CTLE, DFE and FFE equalizers, and transmitter emphasis/de-emphasis.

Advanced De-embedding, Emulation and Virtual Probing

The VirtualProbe package expands the de-embedding and emulation capabilities of EyeDoctorII. Configure a multi-block circuit using modeled S-parameters or measured with a Teledyne LeCroy SPARQ (or other VNA), and VirtualProbe will build the transfer function that returns the signal as it would appear before or after any block in the circuit. The electrical behavior of a block to reflect and transmit signals can be included, added or removed in order to de-embed or emulate fixtures or channels. Probe loading effects can also be removed. When used in conjunction with the Crosstalk, CrossLinQ or CompleteLinQ SDAIII options, crosstalk between lanes can be modeled using 8 and 12-port S-parameters. Use the Teledyne LeCroy SPARQ to measure these S-parameters at a fraction of the price of a VNA.

Use EyeDoctorII and VirtualProbe with SDAIII CompleteLinQ products

When using EyeDoctorII and VirtualProbe on oscilloscopes enabled within the SDAIII-CompleteLinQ products, configure de-embedding, emulation and equalization from the same simple flow-chart dialog as all other serial data analysis features. When enabled with the "LinQ" option to enable 4 lanes, users can configure EyeDoctorII and VirtualProbe configurations on each lane, facilitating rapid comparisons of different de-embedding and equalization setups.

Learn More

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SPARO SIGNAL INTEGRITY NETWORK ANALYZER

The SPARQ signal integrity network analyzers connect directly to the device under test (DUT) and to PC-based software through a single USB connection for quick, multi-port S-parameter measurements.

SPARQ is the ideal instrument for characterizing multi-port devices common in signal integrity applications at a fraction of the cost of traditional methods. It is ideal for:

- Development of measurementbased simulation models
- Design validation
- Compliance testing
- High-performance TDR
- PCB testing
- Portable measurement requirements

High-bandwidth, Multi-port S-parameters for the Masses

S-parameter measurements are most often produced by the vector network analyzer (VNA), a difficult instrument that is beyond many



budgets. SPARQ is very affordable and simplifies measurements, making S-parameters accessible to all.

PC-based, Small and Portable

Traditional instruments that produce S-parameters are large and fundamentally stationary. The SPARQ, in contrast, is small and weighs less than 20 lbs. It connects to any standard PC through a USB 2.0 interface, allowing SPARQ to run where computing power is easily upgraded.

S-parameters, Quick

VNA measurements begin with the unpleasant and complex task of calibration. This involves multiple connections that can produce misleading results due to operator

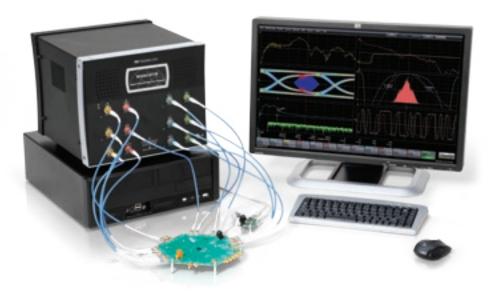
error. The SPARQ provides calibrated measurements with a single connection to the DUT and offers simple setup choices. Start and complete the entire measurement with a single button press.

Internal Calibration

SPARQ takes a revolutionary approach to calibration by building in calibration standards. This enables measurements to be made without multiple connection steps and removes the need for additional electronic calibration (ECAL) modules. Calibration proceeds quickly without user intervention, so one can calibrate often without resorting to the use of out-of-date saved calibrations.

Characterize Crosstalk with 8 and 12-port SPARQs

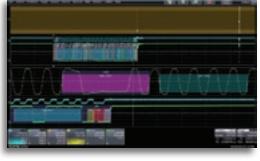
Don't just model crosstalk measure it. With the 8 and 12 port SPARQs, characterize interconnects with two and three differential lanes in order to obtain S-parameters needed for simulations of aggressor/ victim/aggressor topologies.



MOST COMPLETE DEBUG SOLUTION FROM 4-45 GHz

Complete System Debug

Understanding the relationships between different signals is vital to fast debug. Only WaveMaster 8 Zi-A combines the best of general purpose oscilloscopes (low-speed serial triggers and decoders, mixed signal capability, high impedance probing) to allow easy correlation between low-speed (serial data control words, power supply noise, or parallel data transmissions) and high speed events.



Capture 5 ms (100 Mpts) of low-speed and high-speed waveforms. Decode low and high speed serial data signals. Easily zoom, and validate timing relationships between signals.

Get more insight with multiple views of your serial data transmissions.

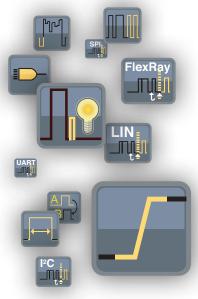
Serial Decode—A Whole New Meaning to Insight

Over 19 different protocols are supported with serial decoders (many with hardware protocol triggers as well). Use ProtoSync with PCIe, USB, SATA, SAS, and Fibre Channel to get a dual-display view of both oscilloscope-generated decode annotations and protocol analyzer software views. Search on protocol data in a table and export table data to an Excel file.

Learn More teledynelecroy.com/dl/3005

More Trigger Capability Isolates More Problems More Quickly

15 GHz Edge trigger, up to 14.1 Gb/s true-hardware serial trigger available, 10 different SMART triggers, four-stage Cascade™ triggering, Measurement trigger, and TriggerScan™ are all standard and allow you to isolate the problem quickly and begin to focus on the cause. A full range of protocol serial triggers (I²C, SPI, UART, RS-232, Audio (I²S, LJ, RJ, TDM), CAN, LIN, FlexRay, MIL-STD-1553 and many others) are also available.



15 GHz Edge Trigger

Search and Scan to Understand

Search a captured waveform for hundreds of different measurement parameters or other conditions using WaveScan. Set complex conditions, view search results on the waveform and in a table, and quickly zoom and jump to an entry. "Scan" for events that can't be triggered in hardware.

Freedom from **Probing Limitations**

High bandwidth differential probes (up to 25 GHz), single-ended active probes, current probes, high-voltage, and mixed signals all connect to the WaveMaster 8 Zi-A oscilloscope and give you a total system view. All WaveMaster 8 Zi-A oscilloscopes contain selectable 50 Ω and 1 M Ω input capability and can be used with any Teledyne LeCroy probe—passive or active—without requiring external adapters or power supplies.

Fully Integrated Mixed Signal Oscilloscope (4+36) Option

Add Mixed Signal Oscilloscope (MSO) operation using the MS Series mixed signal options to acquire up to 36 digital lines time-correlated with analog waveforms and completely integrated with the scope operation. In addition to acquiring digital lines, they are also helpful for monitoring low-speed signals, such as serial data clock, data, and chip select signals, thus preserving the analog channels for higher speed requirements.



DEEP INSIGHT CLARIFIES COMPLEX SIGNALS

All Oscilloscope Tools are not Created Equal

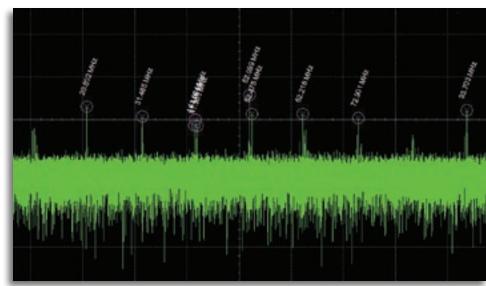
WaveMaster 8 Zi-A has the deepest standard toolbox of any oscilloscope, providing more measure, math, graphing, statistical, and other tools, and more ways to leverage the tools to get the answer faster. While many other oscilloscopes provide similar looking tools, Teledyne LeCroy allows the most flexibility in applying the tools to any waveform.

Customized Tools

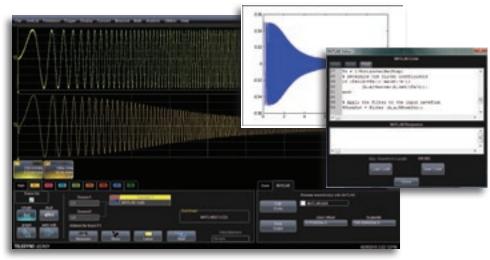
Only Teledyne LeCroy completely integrates third party programs into the scope's processing stream by allowing you to create and deploy a new measurement or math algorithm directly into the oscilloscope environment and display the result on the oscilloscope in real-time! There is no need to run a separate program, or ever leave the oscilloscope window. Use C/C++, MATLAB, Excel, JScript (JAVA), and Visual Basic to create your own customized math functions, measurement parameters, or other control algorithms.

Graphical Track, Trend, and Histogram Views

Track plots measurement values on the Y-axis and time on the X-axis to display a measurement change time-correlated to the original channel acquisition—perfect for intuitive understanding of behaviors in frequency modulated (FM) or pulse width modulated (PWM) circuits and jitter measurements,



X-Stream II fast throughput streaming architecture makes difficult analysis and deep insight possible. Above, an FFT is applied to a 50 Mpts waveform to determine root cause failure. The high frequency resolution this provides enables deep insight into signal pathologies.



XDEV Customization software package being used to implement a 1 MHz Butterworth filter using MATLAB®.

including modulation or spikes. Histograms provide a visual distribution representation of a large sample of measurements, allowing faster insight. Trends are ideal for plotting slow changes in measurement values.



Capture a single clock channel (yellow) and display Track graphs and Histograms simultaneously of multiple jitter parameters.

APPLICATION SPECIFIC SOLUTIONS

In addition to the general purpose WaveShape Analysis tools, application specific solutions are available for Serial Data Compliance, Embedded Design, Digital Design, and Automotive. These packages extend the Teledyne LeCroy standard measurement and analysis capabilities and expand your oscilloscope's utility as your needs change.

Up to 14.1 Gb/s Serial Trigger

The world's fastest oscilloscope true hardware protocol trigger - 600 Mb/s to 14.1 Gb/s! Trigger on up to an 80-bit NRZ serial pattern, or on an 8b/10b symbol string.



ProtoSync Solutions

ProtoSync links physical layer waveforms, data link layer decode annotation and table information, and full transaction layer protocol analysis together. By simply touching a decode table entry in the oscilloscope software or a packet in the protocol analysis software, all views are automatically synchronized and aligned for quick and easy debug. ProtoSync supports PCIe Gen1/2/3, USB2/3, SATA, SAS, and Fibre Channel.



Serial Data Trigger/Decode and PROTObus MAG Serial Debug Toolkit

More than 19 trigger and decode options provide powerful conditional serial data protocol triggering, intuitive color-coded decode overlays, and a table summary with search and zoom capabilities. Additionally, PROTObus MAG (measure, analysis, graph) Serial Debug Toolkit provides the ability to quickly validate and analyze serial data cause-effect relationships and plot digitally encoded data as an analog waveform.

Data Transfer Speeds up to 325 MB/s

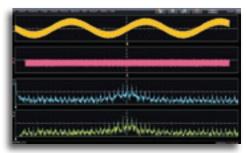
Teledyne LeCroy's Serial Interface Bus (LSIB) option enables direct connection to the PCI Express® x4 high-speed data bus in the oscilloscope to enable

data transfer rates up to 325 MB/s—20–100x faster than other methods. All that is required is installation of an optional LSIB card in the oscilloscope and the corresponding host board (card) for desktop (laptop) PC in the remote computer. Data transfer is easily enabled through a supplied application program interface (API).



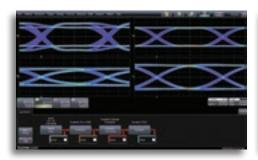
Serial Data Compliance Test Solutions

QualiPHY serial data compliance packages provide easy to use step-by-step instructions for a broad set of serial data standards. With fast automated performance, illustrated instructions and comprehensive reporting capability, QualiPHY packages are the best solution for compliance testing. For standards not supported with QualiPHY compliance packages, jitter and eye diagram test toolsets are generally included in the SDA 8 Zi-A models.



Digital Filter Software Package (WM8Zi-DFP2)

Create and apply a variety of FIR and IIR digital filters to your capture waveforms or processed traces.

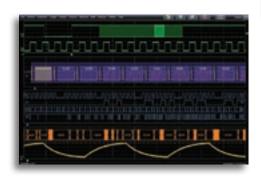


Eye Doctor II—Advanced Signal Integrity Tools (WM8Zi-EYEDRII)

Eye Doctor II and Virtual Probe
Signal Integrity Tools provide the
ability to add precision to signal
integrity measurements by allowing
subtraction of fixture effects and
emulation of emphasis, serial data
channels and provide for receiver
equalization.

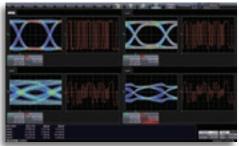
Learn More

teledynelecroy.com/dl/1023 teledynelecroy.com/vid/M0T6WEC0JYQ teledynelecroy.com/dl/1216 teledynelecroy.com/dl/1136



Mixed Signal Oscilloscope Option (MS-250/MS-500)

The Mixed Signal options allow the WaveMaster 8 Zi-A to convert to a mixed signal oscilloscope with up to 36 digital channels with 2 GS/s digital sample rate and 50 Mpts/Ch.



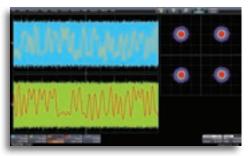
Virtual Probe Signal Integrity Tools (WM8Zi-VIRTUALPROBE)

Provides ability to define a complex serial data channel or other complex topology with up to six circuit elements that may be embedded or de-embedded from the signal path, allowing "probing" at a location different than the measured position.



Spectrum Analyzer Analysis Package (WM8Zi-SPECTRUM)

SPECTRUM converts the controls of your oscilloscope to those of a spectrum analyzer. Adjust the frequency span, resolution and center frequency. Apply filtering to your signal and watch the frequency signature change in real time. A unique peak search labels spectral components and presents frequency and level in a table. Touch any line to move to that peak.



Optical Coherent Modulation Analysis

For Optical Coherent Modulation analysis at 112 Gb/s (28 GBaud) or higher, Teledyne LeCroy's 8 Zi-A oscilloscopes are the ideal digital acquisition system. 20 GHz four channel models (820Zi-A) with upgrade paths to 30 or 45 GHz are an economical and versatile solution for 28 GBaud testing. For > 28 GBaud testing, Teledyne LeCroy provides a variety of bandwidth and channel count solutions at 30, 36, 50, 60, and 65 GHz. Consult Teledyne LeCroy for more details.

Learn More

teledynelecroy.com/dl/1314 teledynelecroy.com/dl/3005

HIGH BANDWIDTH PROBING SOLUTIONS

Ultra-wideband Architecture for Superior Signal Fidelity

Teledyne LeCroy's WaveLink® high bandwidth differential probes utilize advanced differential traveling wave (distributed) amplifier architecture to achieve superior high frequency analog broadband performance.

Highest Bandwidth (25 GHz) Solder-In Lead

Up to 25 GHz Solder-In performance with system (probe + oscilloscope) rise times equal to that of the oscilloscope alone.

Ultra-compact Positioner (Browser) Tip

The most compact positioner tip browser with bandwidth up to 22 GHz makes probing in confined areas easy.

Superior Probe Impedance Minimizes Circuit Loading

Circuit and signal loading is reduced by more than 50% with WaveLink high bandwidth probes compared to competitive probes. In the mid-band frequency range, the difference is even more apparent.

Superior Signal Fidelity and Lowest Noise

WaveLink has exceptional noise performance. In fact, the combination of the probe and the oscilloscope results in measurement performance that is nearly identical to that of a cable input.



D2505-A-PS 25 GHz probe system with Solder-In lead and browser positioner tip.

	D1305-A, D1305-A-PS	D1605-A, D1605-A-PS	D2005-A, D2005-A-PS	D2505-A, D2505-A-PS		
Bandwidth	Dxx05-SI and Dxx05-PT Tips 13 GHz	Dxx05-SI and Dxx05-PT Tips	Dxx05-SI and Dxx05-PT Tips	Dxx05-SI Lead 25 GHz		
	13 GHZ	16 GH2	20 GH2	Dxx05-PT Tip 22 GHz typical 20 GHz guaranteed		
Rise Time (10-90%)	Dxx05-SI and Dxx05-PT Tips	Dxx05-SI and Dxx05-PT Tips	Dxx05-SI and Dxx05-PT Tips	Dxx05-SI Lead 17.5 ps (typical)		
	32.5 ps (typical)	28 ps (typical)	20 ps (typical)	Dxx05-PT Tip 19 ps (typical)		
Rise Time (20–80%)	Dxx05-SI and Dxx05-PT Tips 24.5 ps (typical)	Dxx05-SI and Dxx05-PT Tips 21 ps (typical)	Dxx05-SI and Dxx05-PT Tips 15 ps (typical)	Dxx05-SI Lead 13 ps (typical) Dxx05-PT Tip 14 ps (typical)		
Noise (Probe)	< 14 nV/√Hz (1.6 mV _{rms}) (typical)	< 14 nV/√Hz (1.8 mV _{rms}) (typical)	< 18 nV/√Hz (2.5 mV _{rms}) (typical)	< 18 nV/√Hz (2.8 mV _{rms}) (typical)		
Input Dynamic Range		2.0 V _{pk-pk} , (±1	.0 V) (nominal)			
Input Common Mode Voltage Range	±4 V (nominal)					
Input Offset Voltage Range	±2.5 V Differential (nominal)					
Impedance (mid-band, typical)	600	x05-SI Lead: 300 Ω a Ω at 16 GHz, 300 Ω a x05-PT Tip: 160 Ω at	at 20 GHz, 120 Ω at 2	5 GHz		

240 Ω at 16 GHz, 210 Ω at 20 GHz



Dxx30-PS Differential Probe Systems

Available in 8, 10, and 13 GHz, the Dxx30 models have an optional SMA/SMP lead set for attaching to the device under test (DUT). Additionally, solder-in, positioner (browser) tip, and square pin leads are available.

BROAD RANGE OF PROBING SOLUTIONS

WaveMaster 8 Zi-A oscilloscope support a broad range of probes for a variety of applications.

ZS Series High Impedance Active Probes

- 1 GHz (ZS1000), 1.5 GHz (ZS1500) and 2.5 GHz (ZS2500) bandwidths
- High Impedance (0.9 pF, 1 M Ω)
- Extensive standard and available probe tip and ground connection accessories
- ±12 Vdc offset (ZS1500)
- Teledyne LeCroy ProBus system



High-Voltage Passive Probes

- Suitable for safe, accurate high-voltage measurements
- 1.2 kV to 20 kV
- Works with any 1 $M\Omega$ input oscilloscope



Current Probes

- Range of probes from 30 $A_{\rm rms}$ (50 $A_{\rm peak}$) to 500 $A_{\rm rms}$ (700 $A_{\rm peak}$)
- 2 MHz to 100 MHz bandwidths
- Small form factor accommodates large conductors with small jaw size
- Teledyne LeCroy ProBus system



ZD Series Differential Probes

- 200 MHz, 500 MHz, GHz
- and 1.5 GHz bandwidths
- Wide range of probing accessories
- Teledyne LeCroy ProBus system



High-Voltage Differential Probes

- 20 MHz and 100 MHz bandwidth
- 1,000 V_{rms} common mode voltage
- 1,400 V_{peak} differential voltage
- EN 61010 CAT III
- 80 dB CMRR at 50/60 Hz
- Teledyne LeCroy ProBus system

WaveLink Differential Probes

y ProBus system

- 4 and 6 GHz models
- Solder-In, Browser, Quick Connect, Square Pin, Positioner Tip and HiTemp Cables



WaveLink Medium Bandwidth Differential Probes

- 8, 10, and 13 GHz models
- 3.5 V_{n-n} Input Dynamic Range
- ±4 V Offset
- Solder-in, Positioner (Browser), Square Pin, and SMA/SMP lead connection



Optical-to-Electrical Converter (OE695G)

- Frequency range DC to 9.5
 GHz (electrical, -3 dB)
- Reference receiver support from 8GFC to 10GFC FEC, or Custom (<12.5 Gb/s)



- 62.5/125 µm multi-mode or single-mode fiber input
- Broad wavelength range (750 to 1650 nm)
- +7 dBm (5 mW) max peak optical power
- Low noise (as low as 25 pW/√Hz)

Vertical System	WaveMaster 804Zi-A (SDA)	WaveMaster 806Zi-A (SDA)	WaveMaster 808Zi-A (SDA/DDA)	WaveMaster 813Zi-A (SDA)	WaveMaster 816Zi-A (SDA)	WaveMaster 820Zi-A (SDA, DDA)
Analog Bandwidth @ 50 Ω (-3 dB) (ProLink Input)	4 GHz (≥ 10 mV/div)	6 GHz (≥ 10 mV/div)	8 GHz (≥ 10 mV/div)	13 GHz (≥ 10 mV/div)	16 GHz (≥ 10 mV/div)	20 GHz (≥ 10 mV/div)
Analog Bandwidth @ 50 Ω (-3 dB) (ProBus Input)	3.5 GHz (≥ 10 mV/div)	3.5 GHz (≥ 10 mV/div)	3.5 GHz (≥ 10 mV/div)	3.5 GHz (≥ 10 mV/div)	3.5 GHz (≥ 10 mV/div)	3.5 GHz (≥ 10 mV/div)
Analog Bandwidth @ 1 MΩ (-3 dB) (ProBus Input)	500 MHz (typical, ≥ 2	mV/div)				
Rise Time (10–90%, 50 Ω)	95 ps (test limit, flatness mode)	63 ps (test limit, flatness mode)	49 ps (test limit, flatness mode)	32.5 ps (test limit, flatness mode)	28.5 ps (test limit, flatness mode)	22 ps (test limit, flatness mode)
Rise Time (20–80%, 50 Ω)	71 ps (flatness mode)	47 ps (flatness mode)	37 ps (flatness mode)	24.5 ps (flatness mode)	21.5 ps (flatness mode)	16.5 ps (flatness mode)
Input Channels	4 (Any combination of	f ProLink and ProBus	inputs)			
Bandwidth Limiters	20 MHz, 200 MHz, 1 GHz	20 MHz, 200 MHz, 1 GHz, 4 GHz	20 MHz, 200 MHz, 1 GHz, 4 GHz, 6 GHz	20 MHz, 200 MHz, 1 GHz, 4 GHz, 6 GHz, 8 GHz	20 MHz, 200 MHz, 1 GHz, 4 GHz, 6 GHz, 8 GHz, 13 GHz	20 MHz, 200 MH 1 GHz, 4 GHz, 6 GHz, 8 GHz, 13 GHz, 16 GH:
Input Impedance			iv, 50 Ω ±3% for > 10 F, 10 M Ω 11 pF with			
Input Coupling	ProLink Inputs: 50 Ω	· DC. GND				
	ProBus Inputs: 1 MΩ		: DC, GND			
Maximum Input Voltage	50 Ω (ProLink): ±2 V 50 Ω (ProBus): ±5 V 1 M Ω (ProBus): 250 V	max., 3.5 V _{rms}		//div		
Channel-Channel Isolation	DC to 10 GHz: 50 dB 10 to 15 GHz: 46 dB 15 to 20 GHz: 40 dB	(> 200:1)				
	(For any two ProLink		or different v/div setti	ngs, typical)		

Vertical System	WaveMaster 825Zi-A (SDA)	WaveMaster 830Zi-A (SDA, DDA)	WaveMaster 845Zi-A (SDA)
Analog Bandwidth @ 50 Ω (-3 dB) (2.4/2.92 mm input)	25 GHz	30 GHz	45 GHz
Analog Bandwidth @ 50 Ω (-3 dB) (ProLink Input)	20 GHz (≥ 10 mV/div)	20 GHz (≥ 10 mV/div)	20 GHz (≥ 10 mV/div)
Analog Bandwidth @ 50 Ω (-3 dB) ProBus Input)	3.5 GHz (≥ 10 mV/div)	3.5 GHz (≥ 10 mV/div)	3.5 GHz (≥ 10 mV/div)
Analog Bandwidth @ 1 MΩ (-3 dB) ProBus Input)	500 MHz (typical, ≥ 2 mV/div)		
Rise Time (10–90%, 50 Ω)	17.5 ps (test limit, flatness mode)	15.5 ps (test limit, flatness mode)	10.5 ps (test limit, flatness mode)
Rise Time (20–80%, 50 Ω)	13 ps (flatness mode)	11.5 ps (flatness mode)	8.0 ps (flatness mode)
Input Channels	ProLink inputs or 3.5	ation of 20 GHz 5 GHz ProBus inputs), or ProBus input), or 2 (@ full BW)	4 (Any combination of 20 GHz ProLink inputs or 3.5 GHz ProBus inputs), 3 (1 @ 30 GHz, 2 with either ProLink or ProBus input), 2 at 30 GHz, 1 at 45 GHz
Bandwidth Limiters	For ≤ 20 GHz Mode: 20 MHz, 200 MHz, 1 GHz, 4 GHz, 6 GHz, 8 GHz, 13 GHz, 16 GHz For > 20 GHz Mode: 20 GHz	For ≤ 20 GHz Mode: 20 MHz, 200 MHz, 1 GHz, 4 GHz 6 GHz, 8 GHz, 13 GHz, 16 GHz For > 20 GHz Mode: 20 GHz, 25 GHz	For ≤ 20 GHz Mode: 20 MHz, 200 MHz, 1 GHz, 4 GHz, 6 GHz, 8 GHz, 13 GHz, 16 GHz For 25 and 30 GHz Mode: 20 GHz, 25 GHz, 30 GHz For 45 GHz Mode: none
Input Impedance	$50~\Omega~\pm2\%$ for $\leq 79~\text{mV/div}$ ProLinI $50~\Omega~\pm2\%$ for $\leq 100~\text{mV/div}$ ProBus	In Inputs: $_{7}$, 50 Ω ±3% for > 79 mV/div k Inputs: $_{7}$, 50 Ω ±3% for > 100 mV/div s Inputs: $_{8}$ M Ω 11 pF with supplied Probe	2.4/2.92 mm Inputs: 50 Ω ±2% for ≤ 79 mV/div, 50 Ω ±3% for > 79 mV/div ProLink Inputs: 50 Ω ±2% for ≤ 100 mV/div, 50 Ω ±3% for > 100 mV/div ProBus Inputs: 50 Ω ±2% or 1 MΩ 16pF, 10 MΩ 11 pF with supplied Probe
nput Coupling	50 Ω: C ProLinl 50 Ω: C ProBus	m Inputs: DC, GND k Inputs: DC, GND s Inputs: ND; 50 Ω: DC, GND	2.4/2.92 mm Inputs: 50Ω : DC, GND ProLink Inputs: 50Ω : DC, GND ProBus Inputs: $1 M\Omega$: AC, DC, 50Ω : DC, GND
Maximum Input Voltage	±2 Vmax @ ≤ 100 mV/div 50 Ω (F ±2 Vmax @ ≤ 100 mV/div 50 Ω (F ±5 Vmax 1 MΩ (I 250 Vmax (peak A	$ 2.92 \text{ mm Inputs:} \\ \pm 2 \text{ Vmax } @ \leq 100 \text{ mV/div, } 5.5 \text{ V}_{rms} @ > 100 \text{ mV/div} \\ \hline 50 \Omega \text{ (ProLink):} \\ \pm 2 \text{ Vmax } @ \leq 100 \text{ mV/div, } 5.5 \text{ V}_{rms} @ > 100 \text{ mV/div} \\ \hline 50 \Omega \text{ (ProBus):} \\ \pm 5 \text{ Vmax, } 3.5 \text{ V}_{rms} \\ \hline 1 \text{ M}\Omega \text{ (ProBus):} \\ \hline 250 \text{ Vmax (peak AC: } < 10 \text{ kHz + DC)} $	
Channel-Channel Isolation	DC to 10 GHz: 50 dB (> 315:1) 10 to 15 GHz: 46 dB (> 200:1) 15 to 20 GHz: 40 dB (> 100:1) 20 GHz to Max BW: 30 dB (> 32:1)	ut channels, same or different v/div se	ttings typical)
Vertical Resolution	8 bits up to 11 bits with enhanced re-		τιπησο, τγρισαι/

WaveMaster WaveMaster **Vertical System** WaveMaster WaveMaster 808Zi-A WaveMaster WaveMaster 820Zi-A 804Zi-A (SDA) 806Zi-A (SDA) (SDA/DDA) 813Zi-A (SDA) 816Zi-A (SDA) (SDA, DDA) (cont'd) Sensitivity 50 Ω (ProLink): 2 mV-1 V/div, fully variable (2-9.9 mV/div via zoom) 50 Ω (ProBus): 2 mV-1 V/div. fully variable 1 MΩ (ProBus): 2 mV-10 V/div, fully variable DC Vertical Gain Accuracy ±1% F.S. (typical), offset at 0 V; ±1.5% F.S. (test limit), offset at 0 V (Gain Component of DC Accuracy) Vertical Noise Floor $1.20 \, \text{mV}_{\text{rms}}$ 1.60 mV_{rms} 1.80 mV_{rms} $1.80 \, \mathrm{mV}_{\mathrm{rms}}$ 1.90 mV_{rms} 2.20 mV_{rms} (50 mV/div) (typical) (typical) (typical) (typical) (typical) (typical) Offset Range 50 Ω (ProLink): ±500 mV @ 2-100 mV/div ±4 V @ > 100 mV/div-1 V/div 50 Ω (ProBus): ±750 mV @ 2-100 mV/div $\pm 4 \text{ V } @ > 100 \text{ mV/div} - 1 \text{ V/div}$ 1 M Ω : ±1 V @ 2-140 mV/div ±10 V @ 142 mV-1.40 V/div ±100 V @ 1.42 V-10 V/div DC Vertical Offset Accuracy ±(1.5% of offset setting + 1.5% F.S. + 1 mV) (test limit) **Horizontal System** Timebases Internal time base common to 4 input channels Time/Division Range 20 ps/div-128 s/div, depending on memory length Real-time Mode: 20 ps/div-64 s/div; RIS Mode: 20 ps/div-10 ns/div; user selectable at ≤ 10 ns/div; Roll Mode: 100 ms/div up to 128 s/div, user selectable at ≥ 100 ms/div and ≤ 5 MS/s Clock Accuracy < 1 ppm + (aging of 0.5 ppm/yr from last calibration) Sample Clock Jitter Up to 10µs Acquired Time Range: 100 fsrms (Internal Timebase Reference) Up to 6.4ms Acquired Time Range: 150 fsrms (Internal Timebase Reference) Delta Time Noise Measurement Accuracy + (Sample Clock Jitter)² (RMS) + (clock accuracy * reading) (seconds) SlewRate Jitter Measurement Noise Floor (Sample Clock Jitter)2 seconds_{rms} (TIE) <500 fs_{rms} Jitter Between Channels <425 fs_{rms} <250 fs_{rms} <450 fs_{rms} <325 fs_{rms} <300 fs_{rms} (TIE, typical, measured at maximum bandwidth) Trigger and < 0.1 ps_{rms} (typical, software assisted), 2 ps_{rms} (typical, hardware) Interpolator Jitter Channel-Channel ±9 x time/div. setting or 25 ns max. (whichever is larger), each channel Deskew Range External Time base 10 MHz; 50 Ω impedance, applied at the rear input Reference (Input) External Time base 10 MHz; 50 Ω impedance, output at the rear Reference (Output)

Vertical System (cont'd)	WaveMaster 825Zi-A (SDA)	WaveMaster 830Zi-A (SDA, DDA)	WaveMaster 845Zi-A (SDA)
Sensitivity	$50~\Omega$ (2.9 10 mV–500 mV/c 50 Ω (Pr 2 mV–1 V/div, fully variable 50 Ω (Pr 2 mV–1 V/div, 1 M Ω (P 2 mV–1 V/div, 1 M Ω (P 2 mV–10 V/div,	liv, fully variable oLink): (2–9.9 mV/div via zoom) oBus): fully variable roBus) fully variable	50 Ω (2.4/2.92 mm): 10 mV-500 mV/div, fully variable 50 Ω (ProLink): 2 mV-1 V/div, fully variable (2-9.9 mV/div via zoom) 50 Ω (ProBus): 2 mV-1 V/div, fully variable 1 MΩ (ProBus): 2 mV-10 V/div, fully variable
DC Vertical Gain Accuracy (Gain Component of DC Accuracy)	$\pm 1\%$ F.S. (typical), offset at 0 V; $\pm 1.5\%$	F.S. (test limit), offset at 0 V	
Vertical Noise Floor (50 mV/div)	2.80 mV _{rms} (typical) 50 Ω (2.9	2.90 mV _{rms} (typical)	3.90 mV _{rms} (typical)
Offset Range	$\pm 500 \text{ mV} \ @ \ 10-79 \text{ mV/div}$ $\pm 4 \text{ V} \ @ \ 80 \text{ mV/div}-500 \text{ mV/div}$ $50 \ \Omega \ (\text{ProLink}):$ $\pm 500 \ \text{mV} \ @ \ 2-100 \ \text{mV/div}$ $\pm 4 \text{ V} \ @ \ >100 \ \text{mV/div}-1 \text{ V/div}$ $50 \ \Omega \ (\text{ProBus}):$ $\pm 750 \ \text{mV} \ @ \ 2-100 \ \text{mV/div}$ $\pm 4 \text{ V} \ @ \ >100 \ \text{mV/div}-1 \text{ V/div}$ $1 \ \text{M}\Omega:$ $\pm 1 \text{ V} \ @ \ 2-128 \ \text{mV/div}$ $\pm 10 \ \text{V} \ @ \ 130 \ \text{mV}-1.28 \ \text{V/div}$ $\pm 100 \ \text{V} \ @ \ 1.3 \ \text{V}-10 \ \text{V/div}$		$\begin{array}{c} \textbf{50} \ \Omega \ (\textbf{2.4/2.92 mm}): \\ \pm 500 \ \text{mV} \ @ \ 10-79 \ \text{mV/div} \\ \pm 4 \ \text{V} \ @ \ 80 \ \text{mV/div} -500 \ \text{mV/div} \\ \textbf{50} \ \Omega \ (\textbf{ProLink}): \\ \pm 500 \ \text{mV} \ @ \ 2-100 \ \text{mV/div} \\ \pm 4 \ \text{V} \ @ \ > 100 \ \text{mV/div} -1 \ \text{V/div} \\ \textbf{50} \ \Omega \ (\textbf{ProBus}): \\ \pm 750 \ \text{mV} \ @ \ 2-100 \ \text{mV/div} \\ \pm 4 \ \text{V} \ @ \ > 100 \ \text{mV/div} -1 \ \text{V/div} \\ \textbf{1} \ \ \textbf{M}\Omega: \\ \pm 1 \ \text{V} \ @ \ 2-128 \ \text{mV/div} \\ \pm 10 \ \text{V} \ @ \ 130 \ \text{mV} -1.28 \ \text{V/div} \\ \pm 100 \ \text{V} \ @ \ 1.3 \ \text{V} -10 \ \text{V/div} \\ \end{array}$
OC Vertical Offset Accuracy Horizontal System	\pm (1.5% of offset setting + 1.5% F.S. +	1 mV) (test limit)	
Timebases	Internal time base common to 4 input	channels	
Time/Division Range	For ≥ 25 GHz Mode: Real-time Mode: 20 ps/div–640 µs/div For ≤ 20 GHz Mode: 20 ps/div–128 s/d Real-time Mode: 20 ps/div–64 s/div; RIS Mode: 20 ps/div–10 ns/div, user s: Roll Mode: 100 ms/div up up to 128 s	, depending on memory length liv, depending on memory length electable at ≤10 ns/div;	iv and < 5 MS/s
Clock Accuracy	< 1 ppm + (aging of 0.5 ppm/yr from la		0
Sample Clock Jitter	Up to 10µs Acquired Time Range: 100 Up to 6.4ms Acquired Time Range: 15	fsrms (Internal Timebase Referen	
Delta Time Measurement Accuracy	$\sqrt{2} * \sqrt{\left(\frac{Noise}{SlewRate}\right)^2 + (Sample Clo$	ck Jitter)² (RMS) + (clock accuracy *	reading) (seconds)
Jitter Measurement Floor	$\sqrt{\left(\frac{Noise}{SlewRate}\right)^2 + (Sample Clo$	ck Jitter) ² seconds _{rms} (TIE)	
Jitter Between Channels (TIE, typical, measured at maximum bandwidth)	<250 fs _{rms}		
Trigger and Interpolator Jitter	< 0.1 ps _{rms} (typical, software assisted)	, 2 ps _{rms} (typical, hardware)	
Channel-Channel Deskew Range	$\pm 9 ext{ x time/div. setting or 25 ns max. (w}$		
External Time base Reference (Input)	10 MHz; 50 Ω impedance, applied at the	· 	
External Time base Reference (Output)	10 MHz; 50 Ω impedance, output at th	e rear	

Acquisition System	WaveMaster 804Zi-A (SDA)	WaveMaster 806Zi-A (SDA)	WaveMaster 808Zi-A (SDA/DDA)	WaveMaster 813Zi-A (SDA)	WaveMaster 816Zi-A (SDA)	WaveMaste 820Zi-A (SDA, DDA
Single-Shot Sample Rate/Ch	40 GS/s on 4 Ch		80GS External Interlea			(021,4221
Random Interleaved Sampling (RIS)	200 GS/s for repetitiv	e signals (20 ps/div to	10 ns/div)			
Maximum Trigger Rate	1 000 000 waveforms	,000,000 waveforms/second (in Sequence Mode, up to 4 channels)				
Intersegment Time Maximum Acquisition Memory	1 μs 256 Mpts/Ch	, eeseena (iii eeqaenee	,eac, ap to . ea			
Standard Memory (4 Ch/2 Ch/1 Ch) (Number of Segments)	20M / 20M / 20M (32 (Memory and Sample (2000)		in 1 or 2 Ch mode wi	th use of WM8Zi-2X8	OGS External Interlea	ving Device)
(4 Ch/2 Ch/1 Ch) (Number of Segments)		options, WM8Zi-2X80	32M / 32N (7,50 M-64 Op 64M / 64N (15,00 L-128 Op 128M / 128I (15,00 VL-256 C 256M / 256I (15,00 GS External Interleavin	0) otion: 4 / 64M 00) otion: M / 128M 00) option: M / 256M 00)	SS/s on 1 or 2 Ch with	twice the memc
Acquisition Processing					_	
Averaging			ntinuous averaging to	1 million sweeps		
Enhanced Resolution (ERES)	From 8.5 to 11 bits ve	ertical resolution				
Envelope (Extrema)	· · · · · · · · · · · · · · · · · · ·	of for up to 1 million s	weeps			
nterpolation	Linear or Sin x/x					
Friggering System						
Vodes	Normal, Auto, Single,	and Stop				
Sources	Any input channel, Au	ux, Aux/10, Line, or Fa	st Edge. Slope and le	vel unique to each so	urce (except line trigo	ger)
Coupling Mode	DC, AC, HFRej, LFRe				1 300	-
Pre-trigger Delay			increments of 100 ns)		
Post-trigger Delay			ed at slower time/div s		le	
Hold-off by Time or Events	From 2 ns up to 20 s					
Internal Trigger Range Trigger Sensitivity with Edge Trigger (Ch 1–4) 1.85/2.4/2.92mm Inputs	±4.1 div from center Not Applicable					
Trigger Sensitivity with Edge Trigger (Ch 1–4) ProBus Inputs	2 div @ < 3.5 GHz 1.5 div @ < 1.75 GHz 1.0 div @ < 200 MHz (for DC coupling, ≥ 10					
Trigger Sensitivity with Edge Trigger (Ch 1–4) ProLink Inputs	2 div @ < 4 GHz, 1.5 div @ < 3 GHz, 1.0 div @ < 200 MHz, (for DC, AC, LFRej coupling,	2 div @ < 6 GHz 1.5 div @ < 3 GHz 1.0 div @ < 200 MHz (for DC, AC, LFRej coupling,	2 div @ < 8 GHz 1.5 div @ < 3 GHz 1.0 div @ < 200 MHz (for DC, AC, LFRej coupling,	(for DC, AC, LFRej coupling,	(for D(LFRej co	< 3 GHz 200 MHz C, AC, pupling,
	≥ 10 mV/div, 50 Ω)	≥ 10 mV/div, 50 Ω)	≥ 10 mV/div, 50 Ω)	\geq 10 mV/div, 50 Ω)	≥ 10 mV/d	aiv, 50 52)

Acquisition System	WaveMaster 825Zi-A (SDA)	WaveMaster 830Zi-A (SDA, DDA)	WaveMaster 845Zi-A (SDA)
Single-Shot Sample Rate/Ch		/s on 4 Ch operated in ≥ 25 GHz Mode)	40 GS/s on 4 Ch (80 GS/s on 2 Ch when operated in ≥ 25 GHz Mode), 120 GS/s on 1 Ch when operated in 45 GHz Mode)
Random Interleaved	For ≥ 25 GHz Mode: Not applicable		
Sampling (RIS)		epetitive signals (20 ps/div to 10 ns/div	V)
Maximum Trigger Rate Intersegment Time	1,000,000 waveforms/second (in Se	equence Mode, up to 4 channels)	
Maximum Acquisition		Mpts/Ch	768 Mpts/Ch
Memory		operation)	(1 Ch operation)
Standard Memory (4 Ch / 2 Ch / 1Ch)	40M / 40M (64M / 64M) (1000)		60M (96M) (1000)
(Number of Segments)			
Memory Options (2 Ch / 1 Ch for 25 or 30 GHz) (1 Ch for 45 GHz) (Number of Segments) Acquisition Processing Averaging Enhanced Resolution (ERES)	S-32 Option (WaveMaster models 64M / 64M (3,500) M-64 Option: 128M / 128M (7,500) L-128 Option: 256M / 256M (15,000) VL-256 Option: 512M / 512M (15,000) Note: In ≤ 20 GHz Modes, reference for WaveMaster 820Zi-A. Summed averaging to 1 million sweed From 8.5 to 11 bits vertical resolutions.	e memory specification seps continuous averaging to 1 million	S-32 Option (WaveMaster models only): 96M (3,500) M-64 Option: 192M (7,500) L-128 Option: 384M (15,000) VL-256 Option: 768M (15,000) Note: In ≤ 30 GHz or ≤ 20 GHz Modes, reference memory specification for WaveMaster 830Zi-A and 820Zi-A respectively.
Envelope (Extrema)	Envelope, floor, or roof for up to 1 n	nillion sweeps	
Interpolation	Linear or Sin x/x		
Triggering System			
Modes	Normal, Auto, Single, and Stop		
Sources		e, or Fast Edge. Slope and level uniqu	e to each source (except line trigger)
Coupling Mode	DC, AC, HFRej, LFRej		
Pre-trigger Delay	0–100% of memory size (adjustable		an in wall are also
Post-trigger Delay	0–10,000 divisions in real time mod From 2 ns up to 20 s or from 1 to 9	e, limited at slower time/div settings of	or in roll mode
Hold-off by Time or Events	110111 2 115 up to 20 \$ 01 from 1 to 9	evenis	
Internal Trigger Range	±4.1 div from center		
Trigger Sensitivity	3 div @ < 15 GHz		
with Edge Trigger (Ch 1–4) 1.85/2.4/2.92mm Inputs	1.5 div @ < 3 GHz 1.0 div @ < 200 MHz (for DC coupli	ng, ≥ 10 mV/div, 50 Ω)	
Trigger Sensitivity with Edge Trigger (Ch 1–4) ProBus Inputs	2 div @ < 3.5 GHz 1.5 div @ < 1.75 GHz 1.0 div @ < 200 MHz (for DC coupling,≥ 10 mV/div, 50 Ω)		
Trigger Sensitivity with Edge Trigger (Ch 1–4) ProLink Inputs	3 div @ < 15 GHz 1.5 div @ < 3 GHz 1.0 div @ < 200 MHz (for DC, AC, LFRej coupling, \geq 10 mV/div, 50 Ω)		

WaveMaster 806Zi-A (SDA)		WaveMaster 808Zi-A (SDA/DDA)	WaveMaster 813Zi-A (SDA)	WaveMaster 816Zi-A (SDA)	WaveMaster 820Zi-A (SDA, DDA)
	! div @ < 1 GHz				
	.5 div @ < 500 MHz				
	.0 div @ < 200 MHz				
	for DC, coupling)				
/ (minimum triggera	1.0 GHz @ ≥ 10 mV/div (mi	le width 200 ps)			
±4 V)	Aux (±0.4 V); Aux/10 (±4 V				
neets slope (positive	riggers when signal meets	negative, or either) ar	nd level condition		
xits a window defin	riggers when signal exits	d by adjustable thresh	olds		
selectable Fields (1	riggers NTSC or PAL with ine or CUSTOM with sele 3:1), or Synch Pulse Slope	8), Lines (up to 2000),			
•	riggers on any input sourcelectable by time or even	d state or edge occurr	red on another input	source. Holdoff betw	een sources is
n mode, triggers re	n Sequence acquisition me s satisfied in the first segn				
out for longer than	riggers if signal drops out	elected time between	1 ns and 20 s		
	ogic combination (AND, N lon't care. The High and Lo				an be high, low, c
av	Exclusion Technology				
	riggers on positive or nega	h widths selectable as	s low as 200 ps to 20	s. or on intermittent	faults
	riggers on positive, negati				
	riggers on intervals select		'	·	
-	riggers on any source if a Holdoff between sources i	-		urce.	
	rigger on positive or negat rigger on edge rates. Sele				nd 20 ns
faults by specifying	rigger on intermittent faul	he expected behavior	and triggering when	that condition is not	met
	iggering				
	Arm on "A" event, then Tri Or Arm on "A" event, ther			"B" event, and Trigge	er on "C" event.
	Cascade A then B: Edge, V Stage B only.			Measurement. Meas	urement can be o
	Cascade A then B then C (Measurement can be on S	idge, Window, Pattern	(Logic), Width, Glitc	h, Interval, Dropout,	or Measurement.
	Cascade A then B then C: Cascade A then B then C t		or Measurement. M	easurement can be c	n Stage D only.
d B, B and C, C and	Holdoff between A and B,) is selectable by time	(1ns to 20s) or numb	per of events.	
	Measurement trigger selectorior stage and the last stage	tage in a Cascade pre	cludes a holdoff sett	ing between the	
	ocol Triggering				
80B-8B10B-TD: 600	Option WM8Zi-6GBIT-80B-	√b/s to 6.5 Gb/s, Char	nnel 4 input only		
	Option WM8Zi-14GBIT-80E				
	Standard on SDA models: Option SDA8Zi-UPG-14GB			only)	
	35. 32. 31 31 3 1 400		, _, =, =	,	
	80 bits NRZ, eight 8b/10b s				
covery outputs prov	lo Clock and Data Recove	ed			
ptional)	ocol Triggering (Optio				
p	ocol Triggering (Op	tional)	otional)		otional)

Measurement Trigger

Select from a large number of measurement parameters trigger on a measurement value with qualified limits. Can be used as only trigger or last event in a Cascade Trigger.

Measurement Trigger

Triggering System (cont'd)	WaveMaster 825Zi-A (SDA)	WaveMaster 830Zi-A (SDA, DDA)	WaveMaster 845Zi-A (SDA)
External Trigger Sensitivity	2 div @ < 1 GHz		
(Edge Trigger)	1.5 div @ < 500 MHz		
	1.0 div @ < 200 MHz		
	(for DC, coupling)		
Max. Trigger Frequency, SMART Trigger	2.0 GHz @ ≥ 10 mV/div (minimum t	riggerable width 200 ps)	
External Trigger Input Range	Aux (±0.4 V); Aux/10 (±4 V)		
Basic Triggers Edge	Triggers when signal meets slope (positive, negative, or either) and level con	dition
			uition
Nindow	Triggers when signal exits a window) id
TV-Composite Video	(50 or 60 Hz) and Line or CUSTOM v	ole line and field HDTV (720p, 1080i, 1080 vith selectable Fields (1–8), Lines (up to 200 nch Pulse Slope (Positive or Negative)	
SMART Triggers [™]			
State or Edge Qualified	Triggers on any input source only if sources is selectable by time or ev	a defined state or edge occurred on anot ents	her input source. Holdoff between
Qualified First	In Sequence acquisition mode, trig	gers repeatably on event B only if a define he acquisition. Holdoff between sources i	
Dropout		er than selected time between 1 ns and 2	·
Pattern		R, NOR) of 5 inputs (4 channels and externated Low level can be selected independently	
SMART Triggers with Exclu	usion Technology		
Glitch		ches with widths selectable as low as 200	ps to 20 s, or on intermittent faults
Vidth (Signal or Pattern)		n widths with widths selectable as low as 20	
nterval (Signal or Pattern)	Triggers on intervals selectable bet	ween 1 ns and 20 s	
limeout		ate (or transition edge) has occurred on ar	nother source.
State/Edge Qualified)	Holdoff between sources is 1 ns to	20 s, or 1 to 99,999,999 events	
Runt	Trigger on positive or negative runts	defined by two voltage limits and two time	limits. Select between 1 ns and 20
Slew Rate	Trigger on edge rates. Select limits	for dV, dt, and slope. Select edge limits b	etween 1 ns and 20 ns
Exclusion Triggering	Trigger on intermittent faults by spe	ecifying the expected behavior and trigger	ing when that condition is not met
Cascade (Sequence) Trigge			
Capability		"B" event. Or Arm on "A" event, then Q hen Qualify on "B" then "C" event, and $\c B$	
Гуреѕ	Cascade A then B: Edge, Window, Measurement can be on Stage B o	Pattern (Logic) Width, Glitch, Interval, Dronly.	pout, or Measurement.
	Cascade A then B then C (Measure Measurement. Measurement can	ement): Edge, Window, Pattern (Logic), W be on Stage C only.	idth, Glitch, Interval, Dropout, or
	Cascade A then B then C: Edge, W	'indow, Pattern (Logic).	
	Cascade A then B then C then D: E	dge, Window, Pattern (Logic), or Measure	ement. Measurement can be on
	Stage D only.		
Holdoff	Holdoff between A and B, B and C,	C and D is selectable by time (1ns to 20s	s) or number of events.
	Measurement trigger selection as t prior stage and the last stage.	the last stage in a Cascade precludes a ho	ldoff setting between the
ligh-speed Serial Protocol	Triggering		
Data Rates	Option WM8Zi-6GBIT-80B-8B10B-7	D: 600 Mb/s to 6.5 Gb/s, Channel 4 input	only
	Option WM8Zi-14GBIT-80B-8B10B	-TD: 600 Mb/s to 14.1 Gb/s, Channel 4 inp	out only
	(Standard on SDA models: 600 Mb	signal for triggering when oscilloscope is /s to 6.5 Gb/s, Channel 4 input only.	
Pattorn Lanath		B10B-TD: 600 Mb/s to 14.1 Gb/s, Channe	i 4 input only)
Pattern Length	80 bits NRZ, eight 8b/10b symbols		
Clock and Data Outputs	No Clock and Data Recovery outpu	ts provided	

Select from a large number of measurement parameters trigger on a measurement value with qualified limits. Can be used as only trigger or last event in a Cascade Trigger.

Color Waveform Display	WaveMaster 804Zi-A (SDA)	WaveMaster 806Zi-A (SDA)	WaveMaster 808Zi-A (SDA/DDA)	WaveMaster 813Zi-A (SDA)	WaveMaster 816Zi-A (SDA)	WaveMaster 820Zi-A (SDA, DDA)
Type	Color 15.3" flat panel					
Resolution	WXGA; 1280 x 768 pi	ixels				
Number of Traces	Display a maximum o math traces.	f 16 traces (up to 40	with some software	options). Simultaneous	sly display channel, z	oom, memory and
Grid Styles Waveform Representation	Auto, Single, Dual, Quan Sample dots joined, o		+X-Y, Dual+X-Y			
Integrated Second D	Supports touch scree			islaiisiid	Latter .	
	(Note: touch screen d				DIIITY.	
Processor/CPU						
Type	Intel® Core™ i7-2600	Quad, 2.6 GHz (up to	3.8 GHz in Turbo mo	ode) (or better)		
Processor Memory	8 GB standard for STI 16 GB standard for L- Up to 32 GB optional	, , , , , , , , , , , , , , , , , , , ,		ry options		
Operating System Real Time Clock	Microsoft Windows® Date and time display			ITP support to synchro	onize to precision inte	ernal clocks
Interface	\ \(\lambda \)		1 - C D			
Remote Control Network Communication Standard		ass C (v1.2) Compliar	nt	mand Set		
GPIB Port (Optional)	Supports IEEE – 488.					
LSIB Port (Optional)	Supports PCle Gen1 >			API		
Ethernet Port	Supports 10/100/1000					,
USB Ports	Minimum 6 total (incl.				ces	
External Monitor Port	15 pin D-Type WXGA Includes support for e					
Serial Port Peripheral Bus	Not Available Teledyne LeCroy LBU	IC standard				
•		o standard				
Power Requirements	6					
Voltage	100–240 VAC ±10% a	at 45–66 Hz, 100–120	VAC ±10% at 380-4	20 Hz, Automatic AC	Voltage Selection, Ins	stallation Categor
Max. Power Consumption	975 W / 975 VA					
Environmental						
Temperature (Operating)	+5 °C to +40 °C inclu	ding CD-RW/DVD-RO	0M drive			
Temperature (Non-Operating)	-20 °C to +60 °C					
Humidity	5% to 80% relative h			40.00		
(Operating) Humidity	Upper limit derates to 5% to 95% relative h					
(Non-Operating)	5 % to 95 % relative ii	urnially (non-condens	sing) as tested per ivi	L-FNF-20000F		
Altitude (Operating)	Up to 10,000 ft. (3048	3 m) at or below +25	°C			
Random Vibration (Operating)	0.5 g _{rms} 5 Hz to 500 H	Hz, 15 minutes in each	h of three orthogonal	axes		
Random Vibration (Non-Operating)	2.4 g _{rms} 5 Hz to 500 H	Hz, 15 minutes in eac	h of three orthogonal	axes		
Functional Shock	20 g _{peak} , half sine, 11	ms pulse, 3 shocks (p	ositive and negative) i	n each of three orthogo	onal axes, 18 shocks t	otal
Physical Dimension	s					
Dimensions (HWD)	14" H x 18.4" W x 16"	D (355 x 467 x 406 m	nm) height excludes f	eet		
Weight	51.5 lbs. (23.4 kg)					
Shipping Weight	70 lbs. (31.8 kg)					
Certifications	CE Canadiant III	al II liatadıf	to FN 61220 FN 6464	0.1 FNG1010 0.000 L	II 61010 1 2-4 - 450	and
	CE Compliant, UL and CSA C22.2 No. 61010-		ιυ ⊑Ν 61326, EN 6101	u-i, eindiuiu-2-030, U	ı∟ o i u i u-i 3rd edition	, and
Warranty and Service						
	3-year warranty calibr		,	1 19 3		
	Lintianal carriag prog	rame incluida avtanda	a warrantu unaradad	and calibration service	000	

Optional service programs include extended warranty, upgrades, and calibration services

Color Waveform Display	WaveMaster 825Zi-A (SDA)	WaveMaster 830Zi-A (SDA, DDA)	WaveMaster 845Zi-A (SDA)
Туре	Color 15.3" flat panel TFT-Active Ma	trix LCD with high resolution touch scree	n
Resolution	WXGA; 1280 x 768 pixels		
Number of Traces	Display a maximum of 16 traces (up memory and math traces.	to 40 with some software options). Simu	ultaneously display channel, zoom,
Grid Styles	Auto, Single, Dual, Quad, Octal, X-Y	, Single+X-Y, Dual+X-Y	
Naveform Representation	Sample dots joined, or sample dots	only	
ntegrated Second Display			
		f user-supplied second display with split- nd display may not be a Fujitsu driver)	grid capability.
Processor/CPU			
Гуре	Intel® Core™ i7-2600 Quad, 2.6 GH	z (up to 3.8 GHz in Turbo mode) (or better	-)
Processor Memory	8 GB standard for STD memory (20	Mpt), S-32 and M-64 memory options	
,	16 GB standard for L-128 and VL-25		
	Up to 32 GB optional	, ,	
Operating System	Microsoft Windows® 7 Professional	Edition (64-bit)	
Real Time Clock		rm an in hardcopy files. SNTP support to sy	nchronize to precision internal clock
nterface			
Remote Control	Via Windows Automation or via Tol	edyne LeCroy Remote Command Set	
Network Communication	VXI-11 or VICP, LXI Class C (v1.2) Co		
Standard	VXI 11 01 VICI, EXI CId33 C (V1.2) C	Simpliant	
GPIB Port (Optional)	Supports IEEE – 488.2		
SIB Port (Optional)	Supports PCIe Gen1 x4 protocol with	h Teledyne LeCroy supplied API	
Ethernet Port	Supports 10/100/1000BaseT Ethern		
JSB Ports		USB 2.0 ports support Windows compat	ible devices
External Monitor Port		support customer-supplied external mon	itor.
	Includes support for extended desk	top operation with second monitor.	
Serial Port Peripheral Bus	Not Available Teledyne LeCroy LBUS standard		
Power Requirements Voltage		00–120 VAC ±10% at 380–420 Hz, Autom	atic AC Voltage Selection,
Max. Power Consumption	Installation Category II 1025 W / 1025 VA		
viax. Fower Consumption	1025 VV / 1025 VA		
Environmental			
Temperature	+5 °C to +40 °C including CD-RW/D	VD-ROM drive	
Operating)			
[emperature	–20 °C to +60 °C		
Non-Operating)	5% to 80% relative humidity (non-c	andanaina) un ta 121 °C	
Humidity Operating)		humidity (non-condensing) at +40 °C	
Humidity		ondensing) as tested per MIL-PRF-28800	 F
Non-Operating)	570 to 5570 relative flaminarty (flori c	oridonality da teated per will trit 20000	•
Altitude	Up to 10.000 ft. (3048 m) at or below	w +25 °C	
Operating)			
Random Vibration	0.5 g _{rms} 5 Hz to 500 Hz, 15 minutes	in each of three orthogonal axes	
Operating)			
Random Vibration	2.4 g _{rms} 5 Hz to 500 Hz, 15 minutes	in each of three orthogonal axes	
Non-Operating)			
Functional Shock	20 g _{peak} , half sine, 11 ms pulse, 3 sh	ocks (positive and negative) in each of thre	e orthogonal axes, 18 shocks total
Physical Dimension			
Dimensions (HWD)	14" H x 18.4" W x 16" D (355 x 467 x	< 406 mm) height excludes feet	
Weight		. (26.4 kg)	60 lbs. (27.2 kg)
Shipping Weight		s. (34.5 kg)	78 lbs. (35.5 kg)
Certifications			
Jo. Cilloutio/10	CE Compliant, UL and cUL listed; cor	forms to EN 61326, EN 61010-1, EN61010	-2-030, UL 61010-1 3rd edition, and
	CSA C22.2 No. 61010-1-12	•	·
Narranty and Service			
-	3-year warranty calibration recomme	ended annually.	
	Ontional contine programs include a	utandad warrantu warradaa and aalibrat	

Standard

Math Tools

Display up to 8 math function traces (F1-F8). The easy-to-use graphical interface simplifies setup of up to two operations on each function trace, and function traces can be chained together to perform math-on-math.

absolute value integral

average (summed) interpolate (cubic, quadratic, sinx/x)

average (continuous) invert (negate) correlation log (base e) (two waveforms) log (base 10) derivative product (x) deskew (resample) ratio (/) difference (-) reciprocal enhanced resolution

rescale (with units) (to 11-bits vertical) roof envelope

sparse exp (base e) square exp (base 10) square root fft (power spectrum, magnitude, sum (+) phase, up to max Mpts) zoom (identity)

Measure Tools

Display any 12 parameters together with statistics, including their average, high, low, and standard deviations. Histicons provide a fast, dynamic view of parameters and wave shape characteristics. Parameter Math allows addition, subtraction, multiplication, or division of two different parameters.

level @ x amplitude area maximum std. deviation base mean aot cycles median width data minimum median phase delav narrow band phase Δ delay

time @ minimum (min.) narrow band power duty cycle number of points time @ maximum (max.) Δ time @ level duration + overshoot falltime (90-10%, Δ time @ level overshoot 80-20%, @ level) from trigger peak-to-peak frequency x @ max. period first x @ min. risetime (10-90%,

20-80%, @ level)

Pass/Fail Testing

last

Simultaneously test multiple parameters against selectable parameter limits or pre-defined masks. Pass or fail conditions can initiate actions including document to local or networked files, e-mail the image of the failure, save waveforms, send a pulse out at the front panel auxiliary BNC output, or (with the GPIB option) send a GPIB SRQ.

Basic Jitter and Timing Analysis Tools

This package provides toolsets for displaying parameter values vs. time, statistical views of parameters using histograms, and persistence view math functions. These tools include:

"Track" graphs of all parameters, no limitation of number

- Cycle-Cycle Jitter - Period @ level - Setup - N-Cycle - Half Period – Hold - N-Cycle with - Width @ level – Skew

start selection - Time Interval - Duty Cycle @ level - Frequency @ level Error @ level - Duty Cycle Error

- Histograms expanded with 19 histogram parameters and up to 2 billion events
- Trend (datalog) of up to 1 million events
- Track graphs of all parameters

Persistence histogram, persistence (range, sigma)

Standard (cont'd)

Advanced Customization

Provides capability to create a math function or measurement parameter in MATLAB, Excel, C++, JavaScript, or Visual Basic Script (VBS) format and insert it into the oscilloscope's processing stream. All results are processed and displayed on the oscilloscope grid, and are available for further processing. Also permits the creation of customized plug-ins that can be inserted into the scope user interface, control of the scope via Visual Basic scripts embedded in customized functions, and use of Teledyne LeCroy's Custom DSO capabilities.

Software Options

SDAIII Serial Data Analysis Software (WM8Zi-SDAIII) (Included in WM8Zi-SDAIII option, Standard on SDA 8 Zi-A and DDA 8 Zi-A Models)

Total Jitter

A complete jitter measurement and analysis toolset with the SDAIII-CompleteLinQ user interface framework. The CompleteLinQ framework provides a single user interface for "LinQ", "Crosstalk", "EyeDrII" and "Virtual Probe" capabilities (purchased separately).

SDAIII provides complete serial data and clock jitter and eye diagram measurement and analysis capabilities. Eye Diagrams with millions of UI are quickly calculated from up to 512 Mpt records, and advanced tools may be used on the Eye Diagram to aid analysis. Complete TIE and Total Jitter (Tj) parameters and analysis functions are provided. Comparison of eye diagrams and jitter analysis between captured lanes and one "reference" location is provided.

- Time Interval Error (TIE) Measurement Parameter, Histogram, Spectrum and Jitter Track
- Total Jitter (Tj) Measurement Parameter, Histogram
- Spectrum
- Eye Diagram Display (sliced)
- Eye Diagram IsoBER (lines of constant Bit Error Rate)
- Eye Diagram Mask Violation Locator
- Eye Diagram Measurement Parameters

- Eve Height - Eye Width - Mask hits - One Level - Eye Crossing - Mask out - Zero Level - Avg. Power - Bit Error Rate - Eye Amplitude - Extinction Ratio - Slice Width (setting)

- Q-Fit Tail Representation
- Bathtub Curve
- Cumulative Distribution Function (CDF)
- PLL Track

Jitter Decompostion Models

Three dual-dirac jitter decomposition methods are provided for maximum measurement flexibility. Q-Scale, CDF, Bathtub Curve, and all jitter decomposition measurement parameters can be displayed using any of the three methods

- · Spectral, Rj Direct
- Spectral, Rj+Dj CDF Fit
- NQ-Scale

Random Jitter (Rj) and Non-Data Dependent Jitter (Rj+BUj) Analysis

- Random Jitter (Ri) Meas Param
- Rj+BUj Spectrum
- Periodic Jitter (Pj) Meas Param
- Rj+BUj Track
- Rj+BUj Histogram
- Pj Inverse FFT

Deterministic Jitter (Dj) Analysis

• Deterministic Jitter (Dj) Measurement Parameter

Software Options (cont'd)

SDAIII Serial Data Analysis Software (continued)

Data Dependent Jitter (DDj) Analysis

- Data Dependent Jitter (DDj) Param
- Duty Cycle Distortion (DCD) Param
- InterSymbol Interference (ISI) Param
- Digital Pattern display
- DDj Plot (by Pattern or N-bit Sequence)
- DDj Histogram
- ISI Plot (by Pattern)

Reference Lane

 Compare current acquisition to Reference with a side-by-side or single (tabbed) display mode

SDAIII "LinQ" Capability

(SDAIII-LinQ, SDAIII-CrossLinQ, and SDAIII-CompleteLinQ Options)

In addition to all SDAIII capabilities, "LinQ" options includes 4 lanes of simultaneous serial data analysis plus the reference lane. If EyeDrII or VirtualProbe are purchased with SDAIII "LinQ" capability, then those capabilities are provided for all four lanes.

Lanescape Comparison Mode

When multiple lanes are enabled for display, Lanescape Comparison Modes is used. Selections for this mode are as follows:

- Single: One lane is displayed at a time.
- Dual: Two lanes are selected for display.
- Mosaic: All enabled lanes are displayed.

SDAIII "Crosstalk" Capability (Included in SDAIII-Crosstalk and SDAIII-CrossLinQ Options)

In addition to all SDAIII capabilities, "Crosstalk" options add the following noise and crosstalk measurements and analysis tools:

- Total, Random and Deterministic noise (Tn, Rn, Dn) measurements
- Breakdown of Dn into InterSymbol Interference noise (ISIn) and Periodic noise (Pn)
- Noise-based eye height and width: EH(BER) and EW(BER)
- Random noise (Rn) + Bounded Uncorrelated noise (BUn) Noise Histogram
- Q-fit for Noise Histogram
- Rn+BUn Noise Spectrum and Peak threshold
- Pn Inverse FFT Plot
- Rn+BUn Noise Track
- Crosstalk Eye Contour Plot

SDAIII-CompleteLinQ

The ultimate in serial data single or multi-lane link analysis. Provides all the capabilities mentioned above in SDAIII, "LinQ", and "Crosstalk", and also includes EyeDrII and Virtual Probe capabilities.

Eye Doctor II Advanced Signal Integrity Tools (WM8Zi-EYEDRII)

Complete set of channel emulation, de-embedding and receiver equalization simulation tools. Provides capability to emulate a serial data link, de-embed or embed a fixture, cable or serial data channel, add or remove emphasis, and perform CTLE, FFE, or DFE equalization. If purchased with SDAIII, then capabilities are accessed from within the SDAIII-CompleteLinQ user interface framework.

Virtual Probe Signal Integrity Tools (WM8Zi-VIRTUALPROBE)

Provides ability to define a complex serial data channel or topology with up to six circuit elements that may be embedded or de-embedded, allowing "probing" at a location different than the measured position. If purchased with SDAIII and EyeDrII (or with the EYEDRII-VP or CompleteLinQ options), then capabilities are accessed from within the single SDAIII-CompleteLinQ user interface framework.

Software Options (cont'd)

Clock and Clock-Data Timing Jitter Analysis Package (WM8Zi-JITKIT)

Provides convenient setup and four views of jitter (statistical, time, spectrum, and overlaid) for a variety of horizontal, amplitude, and timing parameters. Direct display of jitter measurement values. Supports multiple simultaneous views with fast selection of multiple parameter measurements for fast and easy validation.

Cable De-embedding (WM8Zi-CBL-DE-EMBED) (Standard on SDA 8 Zi-A and DDA 8 Zi-A)

Removes cable effects from your measurements. Simply enter the S-parameters or attenuation data of the cable(s) then all of the functionality of the SDA 8 Zi can be utilized with cable effects de-embedded

8b/10b Decode (WM8Zi-8B10B D) (Standard on SDA 8 Zi-A and DDA 8 Zi-A)

Intuitive, color-coded serial decode with powerful search capability enables captured waveforms to be searched for user-defined sequences of symbols. Multi-lane analysis decodes up to four simultaneously captured lanes.

Spectrum Analyzer Mode (WM8Zi-SPECTRUM)

This package provides a new capability to navigate waveforms in the frequency domain using spectrum analyzer type controls. FFT capability added to include:

- Power averaging
- Freq domain parameters
- Power density
- FFT on up to 128 Mpts
- Real and imag components

Disk Drive Measurements Package (WM8Zi-DDM2) (Standard on DDA 8 Zi-A)

This package provides disk drive parameter measurements and related mathematical functions for performing disk drive WaveShape Analysis. Disk Drive Parameters are as follows:

- amplitude assymetry
- local base
- local baseline separation
- local maximum
- local minimum
- local number
- local namber
 local peak-peak
- local time between events
- local time between peaks
- local time between troughs
- local time at minimum
- local time at maximum
- local time peak-trough
- local time over threshold

- local time trough-peak
- local time under threshold
- narrow band phase
- narrow band power
- overwrite
- pulse width 50
- pulse width 50 -
- pulse width 50 +
- resolution
- track average amplitude
- track average amplitude -
- track average amplitude +
- auto-correlation s/n
- non-linear transition shift

ORDER INFORMATION

Product Description	Product Code	Product Description	Product Code
WaveMaster 8 Zi-A Series Oscilloscopes		Memory and Sample Rate Options	
4 GHz, 40 GS/s, 4 Ch, 20 Mpts/Ch WaveMaster with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	WaveMaster 804Zi-A	80 GS/s on 2 Ch Sampling Rate Option for WaveMaster 8 Zi-A (not available for 825Zi-A, 830Zi-	
6 GHz, 40 GS/s, 4 Ch, 20 Mpts/Ch WaveMaster with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	WaveMaster 806Zi-A	or 845Zi-A). Includes two separate external interleav devices with storage case 20 Mpts/Ch Standard Memory for WaveMaster 8 Zi-	
8 GHz, 40 GS/s, 4 Ch, 20 Mpts/Ch WaveMaster with 15.3" WXGA Color Display. 50 Ω and 1 $M\Omega$ Input	WaveMaster 808Zi-A	Includes 8 GB of RAM 32 Mpts/Ch Standard Memory for SDA 8 Zi-A	SDA8Zi-STD
13 GHz, 40 GS/s, 4 Ch, 20 Mpts/Ch WaveMaster with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	WaveMaster 813Zi-A	Includes 8 GB of RAM	
16 GHz, 40 GS/s, 4 Ch, 20 Mpts/Ch WaveMaster with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	WaveMaster 816Zi-A	32 Mpts/Ch Memory Option for WaveMaster 8 Zi-A 64 Mpts/Ch Memory Option for WaveMaster 8 Zi-A	WM8Zi-S-32 WM8Zi-M-64
20 GHz, 40 GS/s, 4 Ch, 20 Mpts/Ch WaveMaster with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	WaveMaster 820Zi-A	64 Mpts/Ch Memory Option for SDA 8 Zi-A 64 Mpts/Ch Memory Option for DDA 8 Zi-A	SDA8Zi-M-64 DDA8Zi-M-64
25 GHz, 80 GS/s, 2 Ch, 40 Mpts/Ch WaveMaster with 15.3* WXGA Color Display. 50 Ω and 1 MΩ Input	WaveMaster 825Zi-A	128 Mpts/Ch Memory Option for WaveMaster 8 Zi-A 128 Mpts/Ch Memory Option for SDA 8 Zi-A	WM8Zi-L-128 SDA8Zi-L-128
(20 GHz, 40 GS/s, 4 Ch, 20 Mpts/Ch)		128 Mpts/Ch Memory Option for DDA 8 Zi-A	DDA8Zi-L-128
30 GHz, 80 GS/s, 2 Ch, 40 Mpts/Ch WaveMaster with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	WaveMaster 830Zi-A	256 Mpts/Ch Memory Option for WaveMaster 8 Zi-A	
(20 GHz, 40 GS/s, 4 Ch, 20 Mpts/Ch)		256 Mpts/Ch Memory Option for SDA 8 Zi-A	SDA8Zi-VL-256
45 GHz, 120 GS/s, 1 Ch, 60 Mpts/Ch WaveMaster with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	WaveMaster 845Zi-A	256 Mpts/Ch Memory Option for DDA 8 Zi-A	DDA8Zi-VL-256
(30 GHz, 80 GS/s, 2 Ch, 40 Mpts/Ch; 20 GHz, 40 GS/s,		CPU, Computer and Other Hardware Op	otions
4 Ch, 20 Mpts/Ch)		Upgrade from 160 GB Hard Drive to 500 GB Hard Drive	rive WM8Zi-500GB-HD
SDA 8 Zi-A Series Serial Data Analyzers		Additional 160 GB Hard Drive. Includes	WM8Zi-160GB-RHD-02
4 GHz, 40 GS/s, 4 Ch, 32 Mpts/Ch Serial Data Analyzer with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	SDA 804Zi-A	Windows [®] 7 OS, Teledyne LeCroy Oscilloscope Software and Critical Scope Operational File Duplicates	
6 GHz, 40 GS/s, 4 Ch, 32 Mpts/Ch Serial Data Analyzer with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	SDA 806Zi-A	Additional 500 GB Hard Drive. Includes Windows 7 OS, Teledyne LeCroy Oscilloscope Software and Critical Scope Operational	WM8Zi-500GB-RHD-02
8 GHz, 40 GS/s, 4 Ch, 32 Mpts/Ch Serial Data Analyzer with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	SDA 808Zi-A	Software and Critical Scope Operational File Duplicates GPIB Option for Teledyne LeCroy Oscilloscope. Half-	- GPIB-2
13 GHz, 40 GS/s, 4 Ch, 32 Mpts/Ch Serial Data Analyzer with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	SDA 813Zi-A	height Card	WM8Zi-8-UPG-16GBRAM
16 GHz, 40 GS/s, 4 Ch, 32 Mpts/Ch Serial Data Analyzer with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	SDA 816Zi-A	8 GB to 32 GB CPU RAM Option	WM8Zi-8-UPG-32GBRAM
20 GHz, 40 GS/s, 4 Ch, 32 Mpts/Ch Serial Data Analyzer with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input	SDA 820Zi-A	Serial Data and CrossTalk Analysis Bundle - Multi-Lane SDA LinQ WM	//8Zi-SDAIII-CompleteLinQ
25 GHz, 80 GS/s, 2 Ch, 64 Mpts/Ch Serial Data Analyzer	SDA 825Zi-A	Framework, including Eye, Jitter, Noise,	SDA8Zi-CompleteLinQ
with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input (20 GHz, 40 GS/s, 4 Ch, 32 Mpts/Ch)		Crosstalk Measurements, with EyeDrII and VirtualProbe	DDA8Zi-CompleteLinQ
30 GHz, 80 GS/s, 2 Ch, 64 Mpts/Ch Serial Data Analyzer	SDA 830Zi-A		WM8Zi-SDAIII-CrossLinQ
with 15.3" WXGA Color Display. 50 Ω and 1 M Ω Input (20 GHz, 40 GS/s, 4 Ch, 32 Mpts/Ch)		Framework, Eye, Jitter, Noise and	SDA8Zi-CrossLinQ
45 GHz, 120 GS/s, 1 Ch, 96 Mpts/Ch Serial Data Analyzer with 15.3" WXGA Color Display, 50 Ω and 1 MΩ Input	SDA 845Zi-A	Crosstalk Measurements Multi-Lane Serial Data Analysis LinQ	DDA8Zi-CrossLinQ WM8Zi-SDAIII-LinQ
(30 GHz, 80 GS/s, 2 Ch, 64 Mpts/Ch; 20 GHz, 40 GS/s,		Framework, Eye and Jitter	SDA8Zi-LinQ
4 Ch, 32 Mpts/Ch)		Measurements	DDA8Zi-LinQ
DDA 8 Zi-A Series Oscilloscopes		Single-Lane Serial Data Analysis	WM8Zi-SDAIII-Crosstalk
8 GHz, 40 GS/s, 4 Ch, 32 Mpts/Ch DDA with	DDA 808Zi-A	Framework, Eye, Jitter, Noise and Crosstalk Measurements	SDA8Zi-Crosstalk DDA8Zi-Crosstalk
15.3" WXGA Color Display. 50 Ω and 1 MΩ Input 20 GHz, 40 GS/s, 4 Ch, 32 Mpts/Ch DDA with 15.3" WXGA Color Display. 50 Ω and 1 MΩ Input	DDA 820Zi-A	Single-Lane Serial Data Analysis Framework, Eye and Jitter Measurements	WM8Zi-SDAIII
30 GHz, 80 GS/s, 2 Ch, 64 Mpts/Ch DDA with	DDA 830Zi-A		
15.3" WXGA Color Display. 50 Ω and 1 M Ω Input		Signal Integrity Toolkits Advanced De-embedding, Emulation	WM8Zi-VIRTUALPROBE
(30 GHz, 80 GS/s, 2 Ch, 64 Mpts/Ch; 20 GHz, 40 GS/s, 4 Ch, 32 Mpts/Ch)		and Virtual Probing Toolkit Signal Integrity Toolkit - Channel & Fixture	WM8Zi-EYEDRII
Included with Standard Configuration		De-embedding/Emulation, Tx/Rx Equalization	VVIVIOZI-LILDINII
÷10, 500 MHz Passive Probe (Qty. 4 on 4-20 GHz units,		Bundle - EyeDrll and VirtualProbe Toolkits	WM8Zi-EYEDRII-VP
Oty. 2 on 25–45 GHz units)) ProLink to SMA Adapter: 4 each (for 4–8 GHz units)	LPA-SMA-A	Cable De-embed Option	WM8Zi-CBL-DE-EMBED
ProLink to K/2.92 mm Adapter: 4 each (for 13–45 GHz un			
Optical 3-button Wheel Mouse, USB 2.0 Protective Front Cover			
Printed Getting Started Manual			
Anti-virus Software (Trial Version)			
Microsoft Windows 7 License			
Commercial NIST Traceable Calibration with Certificate Power Cable for the Destination Country			
E CONTRA LIGITION LOCATION LIQUIDATION LIQUIDATION			

Power Cable for the Destination Country 3-year Warranty

ORDER INFORMATION

Product Description	Product Code
Serial Data Compliance	
QualiPHY Enabled 10GBase-KR Software Option	QPHY-10GBase-KR
QualiPHY Enabled BroadR-Reach Software Option	QPHY-BroadR-Reach
QualiPHY Enabled DDR2 Software Option	QPHY-DDR2
QualiPHY Enabled DDR3 Software Option	QPHY-DDR3
QualiPHY Enabled DisplayPort Software Option	QPHY-DisplayPort
QualiPHY Enabled Ethernet 10/100/1000BT Software Op	otion QPHY-ENET*
QualiPHY Enabled HDMI Software Option	QPHY-HDMI [†]
QualiPHY Enabled LPDDR2 Software Option	QPHY-LPDDR2
QualiPHY Enabled MIPI D-PHY Software Option	QPHY-MIPI-DPHY
QualiPHY Enabled MOST50 ePHY Software Option	QPHY-MOST50
QualiPHY Enabled MOST150 oPHY Software Option	QPHY-MOST150
QualiPHY Enabled PCIe 3.0 Software Option	QPHY-PCle3
QualiPHY Enabled PCIe Gen1 Software Option	QPHY-PCle
QualiPHY Enabled SATA Software Option	QPHY-SATA-TSG-RSG
QualiPHY Enabled SAS-2 Software Option	QPHY-SAS2
QualiPHY Enabled SFI Software Option	QPHY-SFI
QualiPHY Enabled USB 2.0 Software Option	QPHY-USB [‡]
QualiPHY Enabled SuperSpeed USB Transmitter/ Receiver Compliance Software Option	QPHY-USB3-Tx-Rx
*TF-ENET-B required. [†] TF-HDMI-3.3V-QUADPAK required	d. [‡] TF-USB-B required.
PCI Express, SuperSpeed USB (USB 3.0) and SATA Comp Test Solutions are available. Consult Factory.	olete Hardware/Software
0 1 1 5 4 7 4 7 4	

Serial Data Test Fixtures

Oction Butta 100t 1 ixtures	
10/100/1000Base-T Ethernet Test Fixture	TF-ENET-B*
Telecom Adapter Kit 100 Ω Bal., 120 Ω Bal.	, 75 Ω Unbal. TF-ET
HDMI 50Ω Pull-Up Terminator	TF-HDMI-3.3V
HDMI Pull-Up Terminator Quad Pack	TF-HDMI-3.3V-QUADPAK
SATA 1.5 Gb/s, 3.0 Gb/s and 6.0 Gb/s Compliance Test Fixture	TF-SATA-C
SATA 1.5 Gb/s, 3.0 Gb/s and 6.0 Gb/s Compliance Test Fixture Measure Kit	TF-SATA-C-KIT
USB 2.0 Compliance Test Fixture	TF-USB-B
SuperSpeed USB Compliance Test Fixture	TF-USB3
2 x BNC to SMA Adapter	ENET-2ADA-BNCSMA
2 x 18 inch SMA to SMA Cable	ENET-2CAB-SMA018
2 x 36 inch SMA to SMA Cable	ENET-2CAB-SMA036
100 ps Rise Time Filter	RISE-TIME-FILTER-100PS
150 ps Rise Time Filter	RISE-TIME-FILTER-150PS
20 dB SMA Attenuators	20DB-SMA-ATTENUATOR

^{*}Includes ENET-2CAB-SMA018 and ENET-2ADA-BNCSMA

Serial Data Triggers and Decoders

600 Mb/s to 6.5 Gb/s High-speed 80bit NRZ WM8Zi-6GBIT-80B-8B10B-TD and 8b/10b Symbol Serial Pattern Trigger Option for WaveMaster 8 Zi/Zi-A Oscilloscopes and Disk Drive Analyzers. Also includes 8b/10b Decode. (Standard on SDA 8 Zi-A) 600 Mb/s to 14.1 Gb/s 80-bit NRZ WM8Zi-14GBIT-80B-8B10B-TD and 8b/10b Symbol Serial Trigger. SDA8Zi-UPG-14GBIT-80B-8B10B-TD Also includes 8b/10b Decode. 64b/66b Decode Annotation Option WM8Zi-64b66b D 8b/10b Decode Decode Annotation Option WM8Zi-8B10B D ENET Decode Option WM8Zi-ENETbus D Ethernet 10G Decode Option WM8Zi-ENET10Gbus D PCI Express Decode Annotation Option WM8Zi-PCIEbus D USB 3.0 Decode Annotation Option WM8Zi-USB3bus D USB 2.0 Decode Annotation Option WM8Zi-USB2bus D USB2-HSIC Decode Option WM8Zi-USB2-HSICbus D SATA Decode Annotation Option WM8Zi-SATAbus D SAS Decode Annotation Option WM8Zi-SASbus D Fibre Channel Decode Annotation Option WM8Zi-FCbus D D-PHY Decode Option WM8Zi-DPHYbus D

Product Description	Product Code
Serial Data Triggers and Decoders	(cont'd)
DigRF 3G Decode Option	WM8Zi-DigRF3Gbus D
DigRF v4 Decode Option	WM8Zi-DIGRFv4bus D
Audiobus Trigger and Decode Option for I ² S, LJ, RJ, and TDM	WM8Zi-Audiobus TD
Audiobus Trigger, Decode, and Graph Option for I ² S, LJ, RJ, and TDM	WM8Zi-Audiobus TDG
Manchester Decode Option	WM8Zi-Manchesterbus D
MIPI D-PHY Decode Annotation Option	WM8Zi-DPHYbus D
MIPI D-PHY Decode and Physical Layer Tes	t Option WM8Zi-DPHYbus DP
MIPI M-PHY Decode Annotation Option	WM8Zi-MPHYbus D
MIPI M-PHY Decode Annotation and	WM8Zi-MPHYbus DP
Physical Layer Test Option	
I ² C Bus Trigger and Decode Option	WM8Zi-I2Cbus TD
SPI Bus Trigger and Decode Option	WM8Zi-SPIbus TD
LIN Trigger and Decode Option	WM8Zi-LINbus TD
UART and RS-232 Trigger and Decode Option	WM8Zi-UART-RS232bus TD
FlexRay Trigger and Decode Option	WM8Zi-FlexRaybus TD
FlexRay Trigger, Decode, and Physical Layer Test Option	WM8Zi-FlexRaybus TDP
SENT Decode Option	WM8Zi-SENTbus D
CANbus TD Trigger and Decode Option	WM8Zi-CANbus TD
CANbus TDM Trigger, Decode and Measure, Graph Option	/ WM8Zi-CANbus TDM
MIL-STD-1553 Trigger and Decode Option	WM8Zi-1553 TD
ARINC 429 Symbolic Decode Option	WM8Zi-ARINC429bus DSymbolic
PROTObus MAG Serial Debug Toolkit	WM8Zi-PROTObus MAG
Decode Annotation and Protocol Analyzer	WM8Zi-ProtoSync

High-speed Digitizer Output

+ BitTracer Synchronization Software Option

Synchronization Software Option

Decode Annotation and Protocol Analyzer

High-speed PCIe Gen1 x4 Digitizer Output	LSIB-1
PCI Express x1 Host Interface Board for Desktop PC	LSIB-HOSTBOARD
PCI Express x1 Express Card Host Interface for Laptop Express Card Slot	LSIB-HOSTCARD
PCI Express x4 3-meter Cable with x4 Cable Connectors Included	LSIB-CABLE-3M
PCI Express x4 7-meter Cable with x4 Cable Connectors Included	LSIB-CABLE-7M

WM8Zi-ProtoSync-BT

Mixed Signal Testing Options

500 MHz, 2 GS/s, 18 Ch, 50 Mpts/Ch Mixed Signal Oscilloscope Option	MS-500
250 MHz, 1 GS/s, 36 Ch, 25 Mpts/Ch (500 MHz, 18 Ch, 2 GS/s, 50 Mpts/Ch Interleaved) Mixed Signal Oscilloscope Option	MS-500-36
250 MHz, 1 GS/s, 18 Ch, 10 Mpts/Ch Mixed Signal Oscilloscope Option	MS-250

General Purpose and Application Specific Software Options

Spectrum Analysis Option	WM8Zi-SPECTRUM
Digital Filter Software Package	WM8Zi-DFP2
Serial Data Mask Software Package	WM8Zi-SDM
Disk Drive Measurements Software Package	WM8Zi-DDM2
Disk Drive Analyzer Software Package	WM8Zi-DDA
Advanced Optical Recording Measurement Package	WM8Zi-AORM
Electrical Telecom Mask Test Software Package	WM8Zi-ET-PMT
EMC Pulse Parameter Software Package	WM8Zi-EMC
Power Analysis Option	WM8Zi-PWR
Clock Jitter Analysis with Four Views Software Package	WM8Zi-JITKIT

ORDER INFORMATION

Product Description

Product Code

General Accessories

Keyboard, USB	KYBD-1
Probe Deskew and Calibration Test Fixture	TF-DSQ
Hard Carrying Case	WM8Zi-HARDCASE
Soft Carrying Case	WM8Zi-SOFTCASE
Rackmount Accessory for WM8Zi	WM8Zi-RACKMOUNT
ProLink to SMA Adapter	LPA-SMA-A
Kit of ProLink to SMA Adapters	LPA-SMA-KIT-A
ProLink to K/2.92 mm Adapter	LPA-K-A
Kit of ProLink to K/2.92 mm Adapters	LPA-K-KIT-A
Oscilloscope Cart with Additional Shelf and Drawer	OC1024
Oscilloscope Cart	OC1021

Probes and Probe Accessories

Probes and Probe Accessories	
1.5 GHz, 0.9 pF, 1 MΩ High Impedance Active Probe	ZS1500
2.5 GHz, 0.9 pF, 1 M Ω High Impedance Active Probe	ZS2500
200 MHz, 3.5 pF, 1 M Ω Active Differential Probe	ZD200
500 MHz, 1.0 pF, Active Differential Probe	ZD500
1 GHz, 1.0 pF, Active Differential Probe	ZD1000
1.5 GHz, 1.0 pF, Active Differential Probe	ZD1500
WaveLink 4 GHz 2.5 Vp-p Differential Probe System	D410-PS
WaveLink 4 GHz 5 Vp-p Differential Probe System	D420-PS
WaveLink 6 GHz 2.5 Vp-p Differential Probe System	D610-PS
WaveLink 6 GHz 5 Vp-p Differential Probe System	D620-PS
WaveLink 8 GHz 3.5Vp-p Differential Probe System	D830-PS
WaveLink 10 GHz 3.5Vp-p Differential Probe System	D1030-PS
WaveLink 13 GHz 3.5Vp-p Differential Probe System	D1330-PS
WaveLink 13 GHz, 2.0 Vp-p Differential Probe System	D1305-A-PS
WaveLink 16 GHz, 2.0 Vp-p Differential Probe System	D1605-A-PS
WaveLink 20 GHz, 2.0Vp-p Differential Probe System	D2005-A-PS
WaveLink 25 GHz, 2.0 Vp-p Differential Probe System	D2505-A-PS
WaveLink 3GHz Differential Amplifier Module with Adjustable Tip	D300A-AT†
WaveLink 6 GHz Differential Amplifier Module with Adjustable Tip	D600A-AT*
WaveLink ProLink Platform/Cable Assembly (4–6 GHz)	WL-PLink-CASE
WaveLink ProBus Platform/Cable Assembly (4 GHz)	WL-PBus-CASE
SMA/SMP Lead Set for Dxx30 Probes D	xx30-SMA-SMP Leads
Optical-to-Electrical Converter, DC to 9.5 GHz, 785 to 1550 nm	OE695G
7.5 GHz Low Capacitance Passive Probe (\div 10, 1 k Ω ; \div 20	D, 500 Ω) PP066

- * For a complete probe, order a WL-PLink-CASE Platform/Cable Assembly with the Adjustable Tip Module.
- † For a complete probe, order a WL-PBUS-CASE Platform/Cable Assembly with the Adjustable Tip Module

A variety of other active voltage and current probes are also available. Consult Teledyne LeCroy for more information.

Customer Service

Teledyne LeCroy oscilloscopes and probes are designed, built, and tested to ensure high reliability. In the unlikely event you experience difficulties, our digital oscilloscopes are fully warranted for three years and our probes are warranted for one year.

This warranty includes:

- No charge for return shipping
- Long-term 7-year support
- Upgrade to latest software at no charge



1-800-5-LeCroy teledynelecroy.com

Local sales offices are located throughout the world. Visit our website to find the most convenient location.

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